

SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)
Sree Sainath Nagar, Tirupati- 517 102
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Ananthapuramu)

IV B.Tech I Semester Supplementary Examinations November, 2014

**EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS
FOR STUDENTS OF R09 REGULATION READMITTED TO IV B.TECH I SEMESTER (SVEC 10)**

**Date: 25-09-2014
Time: 02.00 PM To 05.00 PM**

DATE /DAY	For 2009 Admitted Batch only
	IT
26/11/2014 WEDNESDAY	ARTIFICIAL INTELLIGENCE 10BT71503
05/12/2014 FRIDAY	EMBEDDED SYSTEMS 10BT70506


PRINCIPAL


Copy to : The Director, SVEC

The HOD of IT : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The A.O, S.V.E.C.

SVEC Canteen. , Estate Manager, Hostel Notice Boards.