



**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
**(Autonomous)**  
Sree Sainath Nagar, Tirupati- 517 102

Date: 06.12.2014

**M.C.A V Semester Regular & Supplementary Examinations**  
**January, 2015.**

**EXAMINATION SCHEDULE**

Time: 02.00 PM To 05.00PM

Date/Day	For 2010,2011 & 2012 Admitted Batches only
27-01-2015 Tuesday	OBJECT ORIENTED ANALYSIS AND DESIGN 10MC50101
29-01-2015 Thursday	MANAGEMENT INFORMATION SYSTEMS 10MC50102
31-01-2015 Saturday	MIDDLEWARE TECHNOLOGIES 10MC50103
03-02-2015 Tuesday	SOFTWARE TESTING 10MC50106
05-02-2015 Thursday	CLOUD COMPUTING 10MC50108

Note: Laboratory examinations will be conducted from 21.01.2015 to 22.01.2015.

  
**CHIEF CONTROLLER OF EXAMINATIONS**

Copy to : The Director, SVEC.

The HOD, M.C.A : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The A.O, S.V.E.C.

The Manager, Andhra Bank Extn. Counter.

Hostel Notice Boards.