

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

MCA V Semester Mid- II examinations will be conducted as per the schedule given below.

## REVISED TIME-TABLE

Time: 02:30 PM to 04:30 PM.

03.01.2015

Date of Exam	Day	Subject
12.01.2015	MON	Object Oriented Analysis and Design <b>10MC50101</b>
13.01.2015	TUE	Management Information Systems <b>10MC50102</b>
17.01.2015	SAT	Middleware Technologies <b>10MC50103</b>
19.01.2015	MON	Software Testing <b>10MC50106</b>
20.01.2015	TUE	Cloud Computing <b>10MC50108</b>

- Note: 1. Students have to bring their college Identity Cards to the examination hall ; otherwise they will not be permitted to write the examination.  
2. Examination will be on the units- V,VI,VII & VIII.

- Copy to: 1) The Director, SVEC.  
2) The HOD, MCA.  
3) A.O for Information.  
4) Examination Section.

  
PRINCIPAL  
