



SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)
Sree Sainath Nagar, Tirupati- 517 102

Date: 27.12.2014

M.C.A I Semester (SVEC14) Regular Examinations
February, 2015

EXAMINATION SCHEDULE

Time: 10.00 AM To 01.00PM

Date/Day	For 2014 Admitted Batch only
16.02.2015 Monday	ACCOUNTING AND FINANCIAL MANAGEMENT 14MC1HS01
18.02.2015 Wednesday	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE 14MC1BS01
20.02.2015 Friday	PROGRAMMING THROUGH C 14MC10101
23.02.2015 Monday	COMPUTER ORGANIZATION 14MC10102
25.02.2015 Wednesday	DATA STRUCTURES 14MC10103
27.02.2015 Friday	PROFESSIONAL COMMUNICATION 14MC1HS02

Note: Laboratory examinations will be conducted from 09.02.2015 to 10.02.2015


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HOD, M.C.A : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The A.O, S.V.E.C.

SVEC Canteen.

Estate Manager.

Hostel Notice Boards.