

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

SREE SAINATH NAGAR, A. RANGAMPET - 517 102.

**II B. Tech. II Semester MID-I Examinations (SVEC10) will be conducted as per the schedule given below:**

## TIME-TABLE

Date: 24/01/2015

DATE /DAY	TIME	CE	EEE	ME	ECE	CSE	EIE	IT	CSSE
<b>09/02/15 MONDAY</b>	10:30 AM to 12:00 NOON	ENVIRONMENTAL SCIENCES 10BT3BS02	ANALOG ELECTRONIC CIRCUITS 10BT40421	FLUID MECHANICS AND HYDRAULIC MACHINERY 10BT30121	ELECTRONIC CIRCUIT ANALYSIS 10BT40401	ENVIRONMENTAL SCIENCES 10BT3BS02	ELECTRONIC CIRCUIT ANALYSIS 10BT40401	ENVIRONMENTAL SCIENCES 10BT3BS02	ENVIRONMENTAL SCIENCES 10BT3BS02
<b>10/02/15 TUESDAY</b>	10:30 AM to 12:00 NOON	STRUCTURAL ANALYSIS - I 10BT40101	NETWORK ANALYSIS AND SYNTHESIS 10BT40201	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY 10BT4HS01	CONTROL SYSTEMS 10BT41301	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY 10BT4HS01	CONTROL SYSTEMS 10BT41301	THEORY OF COMPUTATION 10BT60501	MICRO- PROCESSORS AND INTERFACING 10BT50502
<b>11/02/15 WEDNESDAY</b>	10:30 AM to 12:00 NOON	FLUID MECHANICS- II 10BT40102	ELECTRO MAGNETIC FIELDS 10BT40202	KINEMATICS OF MACHINERY 10BT40301	PULSE AND DIGITAL CIRCUITS 10BT40402	COMPUTER GRAPHICS AND MULTI-MEDIA SYSTEMS 10BT41221	PULSE AND DIGITAL CIRCUITS 10BT40402	DATA COMMUNICATIONS 10BT41201	DESIGN AND ANALYSIS OF ALGORITHMS 10BT50501
<b>12/02/15 THURSDAY</b>	10:30 AM to 12:00 NOON	REINFORCED CEMENT CONCRETE STRUCTURES - I 10BT40103	GENERATION OF ELECTRICAL POWER 10BT40203	THERMAL ENGINEERING - I 10BT40302	ELECTRO MAGNETIC WAVES AND TRANSMISSION LINES 10BT40403	OBJECT ORIENTED PROGRAMMING 10BT40502	TRANSDUCERS IN INSTRUMENTATION 10BT41002	OBJECT ORIENTED PROGRAMMING 10BT40502	OBJECT ORIENTED PROGRAMMING 10BT40502
<b>13/02/15 FRIDAY</b>	10:30 AM to 12:00 NOON	PROBABILITY AND STATISTICS 10BT3BS01	ELECTRICAL MEASUREMENTS 10BT40204	PROBABILITY AND STATISTICS 10BT3BS01	SWITCHING THEORY & LOGIC DESIGN 10BT40404	COMPUTER ARCHITECTURE AND ORGANIZATION 10BT40501	ELECTRICAL AND ELECTRONIC MEASUREMENTS 10BT41001	COMPUTER ARCHITECTURE AND ORGANIZATION 10BT40501	COMPUTER ARCHITECTURE AND ORGANIZATION 10BT40501
<b>14/02/15 SATURDAY</b>	10:30 AM to 12:00 NOON	CONSTRUCTION, PLANNING AND PROJECT MANAGEMENT 10BT40104	TRANSFORMERS AND INDUCTION MACHINES 10BT40205	MANUFACTURING TECHNOLOGY 10BT40303	PRINCIPLES OF ELECTRICAL ENGINEERING 10BT40221	PRINCIPLES OF PROGRAMMING LANGUAGES 10BT40503	PRINCIPLES OF ELECTRICAL ENGINEERING 10BT40221	OPERATING SYSTEMS 10BT50504	OPTIMIZATION TECHNIQUES 10BT4EC01

- Note:**
1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
  2. Examination will be on the Units -I, II, III & IV.
  3. College buses will leave the college by 12:30 PM.

- Copy to:**
1. The Director, SVEC.
  2. All the Heads of Departments for circulation among the staff and to display on notice board.
  3. The A.O., for necessary transport arrangement.

  
**PRINCIPAL**  
