

EXAMINATION SCHEDULE

Date: 14.02.2015

Time: 10.00 AM To 01.00PM

DATE / DAY	For 2014 Admitted Batch only						
	Computer Science (CS) (05)	Electrical Power Systems (EPS) (07)	Software Engineering(SE) (25)	Digital Electronics & Communication Systems (DECS) (38)	VLSI (57)	Communication Systems (CMS) (61)	Computer Networks and Information Security (CN&IS) (63)
09.04.2015 THURSDAY	ADVANCED COMPUTER NETWORKS (14MT10501)	POWER SYSTEM SECURITY AND STATE ESTIMATION (14MT10701)	SOFTWARE DEVELOPMENT METHODOLOGIES (14MT12501)	COMPUTER ARCHITECTURES (14MT13801)	ANALOG IC DESIGN (14MT15701)	ADVANCED DIGITAL SIGNAL PROCESSING (14MT15706)	ADVANCED COMPUTER NETWORKS (14MT10501)
11.04.2015 SATURDAY	ADVANCED DATABASE MANAGEMENT SYSTEMS (14MT10502)	STATIC AND DIGITAL PROTECTION OF POWER SYSTEM (14MT10702)	SOFTWARE PROCESS AND PROJECT MANAGEMENT (14MT12502)	DIGITAL COMMUNICATION TECHNIQUES (14MT13802)	COMPUTATIONAL TECHNIQUES IN MICROELECTRONICS (14MT15702)	DIGITAL COMMUNICATION TECHNIQUES (14MT13802)	INFORMATION SECURITY (14MT20507)
15.04.2015 WEDNESDAY	ADVANCED SOFTWARE ENGINEERING (14MT10503)	ADVANCED POWER SYSTEM STABILITY ANALYSIS (14MT10703)	SOFTWARE REQUIREMENTS AND ESTIMATION (14MT12503)	DIGITAL SYSTEM DESIGN (14MT13803)	DEVICE MODELING (14MT15703)	COMPUTER NETWORKS (14MT13805)	MOBILE COMPUTING (14MT20508)
17.04.2015 FRIDAY	DATA STRUCTURES AND ALGORITHMS (14MT10504)	POWER ELECTRONIC CONVERTERS (14MT10704)	DATA STRUCTURES AND ALGORITHMS (14MT10504)	LINER ALGEBRA (14MT13809)	DIGITAL IC DESIGN (14MT15704)	LINER ALGEBRA (14MT13809)	NETWORK MANAGEMENT (14MT16301)
20.04.2015 MONDAY	DISCRETE STRUCTURES AND GRAPH THEORY (14MT10505)	ADVANCED CONTROL SYSTEMS (14MT10705)	WEB TECHNOLOGIES (14MT12504)	MODERN DIGITAL SIGNAL PROCESSING (14MT13804)	IC FABRICATION (14MT15705)	OPTICAL COMMUNICATIONS AND NETWORKS (14MT23808)	WIRELESS NETWORKS (14MT16302)
22.04.2015 WEDNESDAY	SOFTWARE PROJECT MANAGEMENT (14MT10509)	REACTIVE POWER COMPENSATION AND MANAGEMENT (14MT10707)	CLOUD COMPUTING (14MT12508)	EMBEDDED SYSTEM DESIGN (14MT13807)	LOW VOLTAGE ANALOG CIRCUIT DESIGN (14MT15708)	SATELLITE COMMUNICATIONS (14MT16102)	DATA WAREHOUSING AND DATA MINING (14MT20503)
24.04.2015 FRIDAY	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)

CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HODs, of EEE,ECE,CSE&IT : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The A.O, S.V.E.C. SVEC Canteen. , Estate Manager, Hostel Notice Boards.