

SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)


II B.Tech II Semester Regular/ Supplementary Examinations May, 2015

EXAMINATION SCHEDULE

Date: 11-03-2015

Time: 10.00 AM To 01.00PM

DATE /DAY	For 2010,2011,2012 & 2013 Admitted Batches only									
	CIVIL	E.E.E	ME	E.C.E	C.S.E	E.I.E	I.T	E.Con.E	C.S.S.E	Bio-Tech
04/05/2015 MONDAY	ENVIRONMENTAL SCIENCES 10BT3BS02	ANALOG ELECTRONIC CIRCUITS 10BT40421	FLUID MECHANICS AND HYDRAULIC MACHINERY 10BT30121	ELECTRONIC CIRCUIT ANALYSIS 10BT40401	ENVIRONMENTAL SCIENCES 10BT3BS02	ELECTRONIC CIRCUIT ANALYSIS 10BT40401	ENVIRONMENTAL SCIENCES 10BT3BS02	ELECTRONIC CIRCUIT ANALYSIS 10BT40401	ENVIRONMENTAL SCIENCES 10BT3BS02	MASS TRANSFER AND SEPARATION 10BT42301
06/05/2015 WEDNESDAY	STRUCTURAL ANALYSIS - I 10BT40101	NETWORK ANALYSIS AND SYNTHESIS 10BT40201	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY 10BT4HS01	CONTROL SYSTEMS 10BT41301	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY 10BT4HS01	CONTROL SYSTEMS 10BT41301	THEORY OF COMPUTATION 10BT60501	CONTROL SYSTEMS 10BT41301	MICRO-PROCESSOR AND INTERFACING 10BT50502	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY 10BT4HS01
08/05/2015 FRIDAY	FLUID MECHANICS- II 10BT40102	ELECTRO MAGNETIC FIELDS 10BT40202	KINEMATICS OF MACHINERY 10BT40301	PULSE AND DIGITAL CIRCUITS 10BT40402	COMPUTER GRAPHICS AND MULTI-MEDIA SYSTEMS 10BT41221	PULSE AND DIGITAL CIRCUITS 10BT40402	DATA COMMUNICATIONS 10BT41201	PULSE AND DIGITAL CIRCUITS 10BT40402	DESIGN AND ANALYSIS OF ALGORITHMS 10BT50501	ANALYTICAL TECHNIQUES IN BIOTECHNOLOGY 10BT42302
11/05/2015 MONDAY	REINFORCED CEMENT CONCRETE STRUCTURES - I 10BT40103	GENERATION OF ELECTRIC POWER 10BT40203	THERMAL ENGINEERING - I 10BT40302	ELECTRO MAGNETIC WAVES AND TRANSMISSION LINES 10BT40403	OBJECT ORIENTED PROGRAMMING 10BT40502	TRANSDUCERS IN INSTRUMENTATION 10BT41002	OBJECT ORIENTED PROGRAMMING 10BT40502	TRANSDUCERS IN INSTRUMENTATION 10BT41002	OBJECT ORIENTED PROGRAMMING 10BT40502	MOLECULAR BIOLOGY AND GENETICS 10BT42303
13/05/2015 WEDNESDAY	PROBABILITY AND STATISTICS 10BT3BS01	ELECTRICAL MEASUREMENTS 10BT40204	PROBABILITY AND STATISTICS 10BT3BS01	SWITCHING THEORY & LOGIC DESIGN 10BT40404	COMPUTER ARCHITECTURE AND ORGANIZATION 10BT40501	ELECTRICAL AND ELECTRONIC MEASUREMENTS 10BT41001	COMPUTER ARCHITECTURE AND ORGANIZATION 10BT40501	COMPUTER ARCHITECTURE AND ORGANIZATION 10BT40501	COMPUTER ARCHITECTURE AND ORGANIZATION 10BT40501	BIOCHEMICAL THERMODYNAMICS 10BT42304
15/05/2015 FRIDAY	CONSTRUCTION, PLANNING AND PROJECT MANAGEMENT 10BT40104	TRANSFORMERS AND INDUCTION MACHINES 10BT40205	MANUFACTURING TECHNOLOGY 10BT40303	PRINCIPLES OF ELECTRICAL ENGINEERING 10BT40221	PRINCIPLES OF PROGRAMMING LANGUAGES 10BT40503	PRINCIPLES OF ELECTRICAL ENGINEERING 10BT40221	OPERATING SYSTEMS 10BT50504	PRINCIPLES OF ELECTRICAL ENGINEERING 10BT40221	OPTIMIZATION TECHNIQUES 10BT4EC01	BIOPROCESS ENGINEERING 10BT42305


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC

The HODs, of Civil, ME, EEE, ECE, CSE, EIE, IT, EConE, CSSE & BOT : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The A.O, S.V.E.C.

SVEC Canteen , Estate Manager, Hostel Notice Boards.