

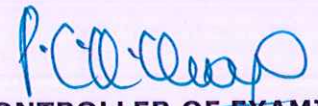
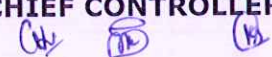
**SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)**  
Sree Sainath Nagar, Tirupati- 517 102  
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

**IV B.Tech I Semester Supplementary Examinations May, 2015**

**EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS  
FOR STUDENTS OF R09 REGULATION READMITTED TO IV B.TECH I SEMESTER (SVEC 10)**

Date: 11-03-2015  
Time: 02.00 PM To 05.00 PM

DATE /DAY	For 2009 Admitted Batch only
	IT
06/05/2015 WEDNESDAY	ARTIFICIAL INTELLIGENCE 10BT71503
15/05/2015 FRIDAY	EMBEDDED SYSTEMS 10BT70506

  
**CHIEF CONTROLLER OF EXAMINATIONS**  


Copy to : The Director, SVEC

The HOD of IT : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.  
The Examination Section.  
The A.O, S.V.E.C.  
SVEC Canteen. , Estate Manager, Hostel Notice Boards.