

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

SREE SAINATH NAGAR, A. RANGAMPET - 517 102.

II B. Tech. II Semester MID-II Examinations (SVEC10) will be conducted as per the schedule given below:

TIME-TABLE

Date: 30/03/2015

DATE /DAY	TIME	CE	EEE	ME	ECE	CSE	EIE	IT	CSSE
13/04/15 MONDAY	03:00 PM to 04:30 PM	ENVIRONMENTAL SCIENCES <i>10BT3BS02</i>	ANALOG ELECTRONIC CIRCUITS <i>10BT40421</i>	FLUID MECHANICS AND HYDRAULIC MACHINERY <i>10BT30121</i>	ELECTRONIC CIRCUIT ANALYSIS <i>10BT40401</i>	ENVIRONMENTAL SCIENCES <i>10BT3BS02</i>	ELECTRONIC CIRCUIT ANALYSIS <i>10BT40401</i>	ENVIRONMENTAL SCIENCES <i>10BT3BS02</i>	ENVIRONMENTAL SCIENCES <i>10BT3BS02</i>
15/04/15 WEDNESDAY	03:00 PM to 04:30 PM	STRUCTURAL ANALYSIS - I <i>10BT40101</i>	NETWORK ANALYSIS AND SYNTHESIS <i>10BT40201</i>	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY <i>10BT4HS01</i>	CONTROL SYSTEMS <i>10BT41301</i>	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY <i>10BT4HS01</i>	CONTROL SYSTEMS <i>10BT41301</i>	THEORY OF COMPUTATION <i>10BT60501</i>	MICRO- PROCESSORS AND INTERFACING <i>10BT50502</i>
16/04/15 THURSDAY	03:00 PM to 04:30 PM	FLUID MECHANICS- II <i>10BT40102</i>	ELECTRO MAGNETIC FIELDS <i>10BT40202</i>	KINEMATICS OF MACHINERY <i>10BT40301</i>	PULSE AND DIGITAL CIRCUITS <i>10BT40402</i>	COMPUTER GRAPHICS AND MULTI-MEDIA SYSTEMS <i>10BT41221</i>	PULSE AND DIGITAL CIRCUITS <i>10BT40402</i>	DATA COMMUNICATIONS <i>10BT41201</i>	DESIGN AND ANALYSIS OF ALGORITHMS <i>10BT50501</i>
17/04/15 FRIDAY	03:00 PM to 04:30 PM	REINFORCED CEMENT CONCRETE STRUCTURES - I <i>10BT40103</i>	GENERATION OF ELECTRICAL POWER <i>10BT40203</i>	THERMAL ENGINEERING - I <i>10BT40302</i>	ELECTRO MAGNETIC WAVES AND TRANSMISSION LINES <i>10BT40403</i>	OBJECT ORIENTED PROGRAMMING <i>10BT40502</i>	TRANSDUCERS IN INSTRUMENTATION <i>10BT41002</i>	OBJECT ORIENTED PROGRAMMING <i>10BT40502</i>	OBJECT ORIENTED PROGRAMMING <i>10BT40502</i>
18/04/15 SATURDAY	03:00 PM to 04:30 PM	PROBABILITY AND STATISTICS <i>10BT3BS01</i>	ELECTRICAL MEASUREMENTS <i>10BT40204</i>	PROBABILITY AND STATISTICS <i>10BT3BS01</i>	SWITCHING THEORY & LOGIC DESIGN <i>10BT40404</i>	COMPUTER ARCHITECTURE AND ORGANIZATION <i>10BT40501</i>	ELECTRICAL AND ELECTRONIC MEASUREMENTS <i>10BT41001</i>	COMPUTER ARCHITECTURE AND ORGANIZATION <i>10BT40501</i>	COMPUTER ARCHITECTURE AND ORGANIZATION <i>10BT40501</i>
20/04/15 MONDAY	03:00 PM to 04:30 PM	CONSTRUCTION, PLANNING AND PROJECT MANAGEMENT <i>10BT40104</i>	TRANSFORMERS AND INDUCTION MACHINES <i>10BT40205</i>	MANUFACTURING TECHNOLOGY <i>10BT40303</i>	PRINCIPLES OF ELECTRICAL ENGINEERING <i>10BT40221</i>	PRINCIPLES OF PROGRAMMING LANGUAGES <i>10BT40503</i>	PRINCIPLES OF ELECTRICAL ENGINEERING <i>10BT40221</i>	OPERATING SYSTEMS <i>10BT50504</i>	OPTIMIZATION TECHNIQUES <i>10BT4EC01</i>

- Note:**
1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
 2. Examination will be on the Units -V, VI, VII & VIII.
 3. College buses will leave the college by 05:00 PM.

- Copy to:**
1. The Director, SVEC.
 2. All the Heads of Departments for circulation among the staff and to display on notice board.
 3. The A.O., for necessary transport arrangement.


PRINCIPAL
