

M. Tech I Semester (SVEC14) Mid-I Examinations will be conducted as per the schedule given below.

TIME-TABLE

Time: 02:45 PM to 04:45 PM

12.11.2015

| DATE /DAY | Computer Science (CS) (05) | Electrical Power Systems (EPS) (07) | Software Engineering(SE) (25) | Digital Electronics and Communication Systems (DECS) (38) | VLSI (57) | Communication Systems (CMS) (61) | Computer Networks and Information Security (CN&IS) (63) |
|---------------------------------|---|--|---|---|---|--|---|
| 23.11.2015 MONDAY | ADVANCED COMPUTER NETWORKS (14MT10501) | POWER SYSTEM SECURITY AND STATE ESTIMATION (14MT10701) | SOFTWARE DEVELOPMENT METHODOLOGIES (14MT12501) | COMPUTER ARCHITECTURES (14MT13801) | ANALOG IC DESIGN (14MT15701) | ADVANCED DIGITAL SIGNAL PROCESSING (14MT15706) | ADVANCED COMPUTER NETWORKS (14MT10501) |
| 24.11.2015 TUESDAY | ADVANCED DATABASE MANAGEMENT SYSTEMS (14MT10502) | STATIC AND DIGITAL PROTECTION OF POWER SYSTEM (14MT10702) | SOFTWARE PROCESS AND PROJECT MANAGEMENT (14MT12502) | DIGITAL COMMUNICATION TECHNIQUES (14MT13802) | COMPUTATIONAL TECHNIQUES IN MICROELECTRONICS (14MT15702) | DIGITAL COMMUNICATION TECHNIQUES (14MT13802) | INFORMATION SECURITY (14MT20507) |
| 25.11.2015 WEDNESDAY | ADVANCED SOFTWARE ENGINEERING (14MT10503) | ADVANCED POWER SYSTEM STABILITY ANALYSIS (14MT10703) | SOFTWARE REQUIREMENTS AND ESTIMATION (14MT12503) | DIGITAL SYSTEM DESIGN (14MT13803) | DEVICE MODELING (14MT15703) | COMPUTER NETWORKS (14MT13805) | MOBILE COMPUTING (14MT20508) |
| 26.11.2015 THURSDAY | DATA STRUCTURES AND ALGORITHMS (14MT10504) | POWER ELECTRONIC CONVERTERS (14MT10704) | DATA STRUCTURES AND ALGORITHMS (14MT10504) | LINER ALGEBRA (14MT13809) | DIGITAL IC DESIGN (14MT15704) | LINER ALGEBRA (14MT13809) | NETWORK MANAGEMENT (14MT16301) |
| 27.11.2015 FRIDAY | DISCRETE STRUCTURES AND GRAPH THEORY (14MT10505) | ADVANCED CONTROL SYSTEMS (14MT10705) | WEB TECHNOLOGIES (14MT12504) | MODERN DIGITAL SIGNAL PROCESSING (14MT13804) | IC FABRICATION (14MT15705) | OPTICAL COMMUNICATIONS AND NETWORKS (14MT23808) | WIRELESS NETWORKS (14MT16302) |
| 28.11.2015 SATURDAY | SOFTWARE PROJECT MANAGEMENT (14MT10509) | REACTIVE POWER COMPENSATION AND MANAGEMENT (14MT10707) | CLOUD COMPUTING (14MT12508) | EMBEDDED SYSTEM DESIGN (14MT13807) | LOW VOLTAGE ANALOG CIRCUIT DESIGN (14MT15708) | SATELLITE COMMUNICATIONS (14MT16102) | DATA WAREHOUSING AND DATA MINING (14MT20503) |
| 30.11.2015 MONDAY | RESEARCH METHODOLOGY (14MT10310) | RESEARCH METHODOLOGY (14MT10310) | RESEARCH METHODOLOGY (14MT10310) | RESEARCH METHODOLOGY (14MT10310) | RESEARCH METHODOLOGY (14MT10310) | RESEARCH METHODOLOGY (14MT10310) | RESEARCH METHODOLOGY (14MT10310) |

Note: 1.Students have to bring their college Identity Cards to the examination hall ; otherwise they will not be permitted to write the examination.

2. Examination will be on the units- I & II,.

Copy to: 1) The Director, SVEC.

2) The HODs of EEE, ECE, CSE & IT with a request to circulate among the students and staff and display on the department notice board.

3) The Examination Section

4) A.O, for information.

PRINCIPAL