



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

Date: 24.11.2015

M.C.A III Semester (SVEC 14) Regular Examinations January, 2016

EXAMINATION SCHEDULE

Time: 10.00 AM To 01.00PM

Date/Day	For 2014 Admitted Batch only
11.01.2016 Monday	Organizational Behavior and Human Resource Management 14MC3HS01
13.01.2016 Wednesday	Operations Research 14MC3BS01
19.01.2016 Tuesday	Object Oriented Analysis and Design 14MC30101
21.01.2016 Thursday	Data Warehousing and Data Mining 14MC30102
23.01.2016 Saturday	Computer Networks 14MC30103

Note: Laboratory examinations will be conducted from 04.01.2016 to 05.01.2016.


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HOD, M.C.A : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The A.O, S.V.E.C.

SVEC Canteen.

Estate Manager.

Hostel Notice Boards.