



# SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

Date: 24.11.2015

## **M.C.A III Semester (SVEC 10) Supplementary Examinations January, 2016**

### EXAMINATION SCHEDULE

Time: 10.00 AM To 01.00PM

Date/Day	For 2010, 2011, 2012 & 2013 Admitted Batches only
11.01.2016 Monday	OPERATIONS RESEARCH 10MC3BS01
13.01.2016 Wednesday	WEB APPLICATION DEVELOPMENT 10MC30101
19.01.2016 Tuesday	COMPUTER NETWORKS 10MC30102
21.01.2016 Thursday	DATA WAREHOUSING AND DATA MINING 10MC30103
23.01.2016 Saturday	SOFTWARE ENGINEERING 10MC30104

Note: Laboratory examinations will be conducted from 04.01.2016 to 05.01.2016.

  
CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HOD, M.C.A : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The A.O, S.V.E.C.

SVEC Canteen.

Estate Manager.

Hostel Notice Boards.