

SREE VIDYANIKETHAN ENGINEERING COLLEGE(Autonomous)

SREE SAINATH NAGAR, A. RANGAMPET - 517 102

2nd SHIFT POLYTECHNIC

SVEC/EC/2015-16/508


21.12.2015

TIME-TABLE

Polytechnic Diploma IV Semester (C14) Unit Test -I Examinations will be conducted as per the schedule given below.

Examination Time:10.30 AM to 11.30 AM

DAY/ DATE	DCE	DEEE	DME	DECE	DCME
	SUBJECTS	SUBJECTS	SUBJECTS	SUBJECTS	SUBJECTS
04.01.2016 Monday	Theory of Structures C-402	Engineering Mathematics - III EE-401	Engineering Mathematics - III M-401	Engineering Mathematics - III EC - 401	Engineering Mathematics -III CM-401
05.01.2016 Tuesday	Engineering Mathematics - III c-401	A.C. Machines - I EE-402	Design of Machine Elements-I M - 402	Linear Integrated Circuits EC - 402	Operating Systems CM-402
06.01.2016 Wednesday	Quantity Surveying - I C-403	Power Systems - I EE-403	Industrial Engineering M - 403	Network Analysis EC - 403	Computer Hardware & Maintenance CM-403
07.01.2016 Thursday	Surveying - III C-404	Electrical Installation and Estimation EE-404	Heat Power Engineering-I M - 404	Digital Communications EC - 404	Microprocessors CM-404
08.01.2016 Friday	Transportation Engineering C-405	Electronics -II EE-405	Fluid Mechanics & Hydraulic Machinery M - 405	Microprocessor & Microcontroller Programming EC - 405	OOPS Through C++ CM-405
09.01.2016 Saturday	--	Programming in C EE-406	Production Technology -II M - 406	Programming in C EC - 406	Computer Networks CM-406


PRINCIPAL
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- Copy to: 1) The Director, SVEC
2) The Vice Principal, 2nd Shift Polytechnic: With a request to arrange to read out in the respective classes and to display on the Notice Board
3) S.A.O. for Information
4) The Exam Section
5) Notice Board