

## M.Tech II Semester (SVEC-10) Supplementary Examinations February , 2016

### EXAMINATION SCHEDULE

Date: 26.12.2015

Examination Time: 10.00 AM To 01.00PM

DATE /DAY	For 2012 & 2013 Admitted Batches only							
	Bio-Technology (BOT)	Computer Science (CS)	Electrical Power Systems (EPS)	Software Engineering(SE)	Digital Electronics and Communication Systems (DECS)	VLSI	Communication Systems (CMS)	Computer networks & information security(CN&IS)
16.02.2016 TUESDAY	Bioreactor Engineering 10MT20301	Operating Systems 10MT20505	Economic Operation & Control of Power Systems 10MT20701	Service Oriented Architecture 10MT22501	Adaptive Signal Processing 10MT23801	Algorithms for VLSI Physical Design Automation 10MT25701	Adaptive Signal Processing 10MT23801	Operating Systems 10MT20505
18.02.2016 THURSDAY	Genetic Engineering 10MT20302	Computer Networks 10MT20501	FACTS Controllers 10MT20702	Software Architecture and Design Patterns 10MT22502	Coding Theory and Techniques 10MT23802	CPLD & FPGA Architectures and Applications 10MT25702	Digital Satellite Communications 10MT26102	Design of Secure Protocols 10MT26301
20.02.2016 SATURDAY	Bioseparation Processes 10MT20303	Data Warehousing and Data mining 10MT20502	HVDC Transmission 10MT20703	Software Reengineering and Reuse 10MT22503	Detection and Estimation of Signals 10MT23803	Digital System Testing and Testability 10MT25703	RF Systems and Circuits 10MT26105	Intrusion Detection Systems 10MT26302
23.02.2016 TUESDAY	Cell Technology 10MT20304	JAVA and Web Technologies 10MT20503	Power System Reliability 10MT20704	Software Security Engineering 10MT22504	Image Processing 10MT23807	Low Power VLSI Design 10MT25704	Image Processing 10MT23807	Wireless Networks 10MT26303
25.02.2016 THURSDAY	Bioinformatics 10MT20305	Object Oriented Analysis and Design 10MT20504	Static and Digital Protection of Power System 10MT20705	Information Retrieval Systems 10MT22506	Wireless Communications 10MT23805	Scripting Language for VLSI Design Automation 10MT25705	Wireless Communications 10MT23805	Information Retrieval Systems 10MT22506
				Software Reliability 10MT22508				Network Programming 10MT26307
27.02.2016 SATURDAY	Advanced Immunotechnology 10MT20306	System Thinking 10MT20509	Energy Audit, Conservation & Management 10MT20706	Software Quality Assurance and Testing 10MT22505	Optical Communications 10MT23808	Nano Electronics 10MT25707	Optical Communications 10MT23808	Cloud Computing 10MT20506
		Cloud Computing 10MT20506			High Performance Networks 10MT23804	Real Time Operating Systems 10MT25708		

**CHIEF CONTROLLER OF EXAMINATIONS**

Copy to : The Director,SVEC..

The HODs, of EEE,ECE,CSE,IT & BOT : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section, S.A.O, SVEC , SVEC Canteen., Estate Manager, Hostel Notice Boards.