

# SREE VIDYANIKETHAN ENGINEERING COLLEGE(Autonomous)

SREE SAINATH NAGAR, A. RANGAMPET - 517 102

## 2<sup>nd</sup> SHIFT POLYTECHNIC

SVEC/EC/2015-16/508

15.02.2016

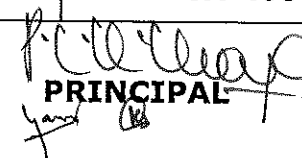
### TIME-TABLE

Polytechnic Diploma IV Semester (C14) Unit Test -II Examinations will be conducted as per the schedule given below.

Examination Time:10.45 AM to 11.45 AM

DAY/ DATE	DCE	DEEE	DME	DECE	DCME
	SUBJECTS	SUBJECTS	SUBJECTS	SUBJECTS	SUBJECTS
05.03.2016 SATURDAY	Theory of Structures C-402	Engineering Mathematics - III EE-401	Engineering Mathematics - III M-401	Engineering Mathematics - III EC - 401	Engineering Mathematics -III CM-401
08.03.2016 TUESDAY	Engineering Mathematics - III c-401	A.C. Machines - I EE-402	Design of Machine Elements-I M - 402	Linear Integrated Circuits EC - 402	Operating Systems CM-402
09.03.2016 WEDNESDAY	Quantity Surveying - I C-403	Power Systems - I EE-403	Industrial Engineering M - 403	Network Analysis EC - 403	Computer Hardware & Maintenance CM-403
10.03.2016 THURSDAY	Surveying - III C-404	Electrical Installation and Estimation EE-404	Heat Power Engineering-I M - 404	Digital Communications EC - 404	Microprocessors CM-404
11.03.2016 FRIDAY	Transportation Engineering C-405	Electronics -II EE-405	Fluid Mechanics & Hydraulic Machinery M - 405	Microprocessor & Microcontroller Programming EC - 405	OOPS Through C++ CM-405
12.03.2016 SATURDAY	--	Programming in C EE-406	Production Technology -II M - 406	Programming in C EC - 406	Computer Networks CM-406

- Copy to: 1) The Director, SVEC  
2) The Vice Principal, SVEC.  
3) The Vice Principal, 2<sup>nd</sup> Shift Polytechnic: With a request to arrange to read out in the respective classes and to display on the Notice Board  
4) S.A.O. for Information  
5) The Exam Section  
6) Notice Board

  
PRINCIPAL