

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, Tirupati – 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

I B.Tech (SVEC-14) Regular/Supplementary Examinations April, 2016

EXAMINATION SCHEDULE

Date: 18-02-2016

Time: 10.00 AM To 01.00PM

DATE /DAY	For 2014 & 2015 Admitted Batches only							
	CIVIL	M.E	E.E.E	E.C.E	E.I.E	C.S.E	I.T	C.S.S.E
25/04/2016 MONDAY	TECHNICAL ENGLISH (14BT1HS01)							
27/04/2016 WEDNESDAY	ENGINEERING PHYSICS (14BT1BS01)							
29/04/2016 FRIDAY	ENGINEERING CHEMISTRY (14BT1BS02)							
02/05/2016 MONDAY	ENGINEERING MATHEMATICS (14BT1BS03)							
04/05/2016 WEDNESDAY	ENGINEERING MECHANICS (14BT10301)		MATHEMATICAL METHODS (14BT1BS04)					
06/05/2016 FRIDAY	PROGRAMMING IN C AND DATA STRUCTURES (14BT1ES01)				PROBLEM SOLVING AND COMPUTER PROGRAMMING (14BT1ES02)			


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The Vice-Principal, SVEC.

The HODs, of CE, M.E, EEE, ECE, CSE, EIE, IT, CSSE & GEBH: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The S.A.O, SVEC; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.