

SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)
Sree Sainath Nagar, Tirupati- 517 102
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Ananthapuramu)

II B.Tech II Semester (SVEC-14) Regular Examinations May, 2016

EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS
FOR STUDENTS OF **SVEC-10** REGULATION READMITTED TO II B.TECH II SEMESTER **SVEC- 14**

Date: 08-03-2016

Time: 10.00 AM To 01.00PM

DATE /DAY	For 2013 Admitted Batch only (SVEC-10)	For 2012 Admitted Batch only (SVEC-10)
	CE	EIE
12/05/2016 THURSDAY	---	SENSORS AND TRANSDUCERS 14BT31002
14/05/2016 SATURDAY	ENGINEERING GEOLOGY 14BT30101	PRINCIPLES OF ELECTRICAL MEASUREMENTS 14BT31001


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC
The Vice-Principal, SVEC.
The HODs of CE & IT : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.
The Examination Section.
The S.A.O, SVEC.
The Manager, SVEC Canteen,
Estate Manager,
Hostel Notice Boards.