

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet – 517 102

M.Tech II Semester (SVEC 14) Mid-I Examinations will be conducted as per the schedule given below.

TIME-TABLE

Time: 02:30 PM to 04:30 PM

13.04.2016

DATE /DAY	Computer Science (CS) (05)	Electrical Power Systems (EPS) (07)	Digital Electronics and Communication Systems (DECS) (38)	VLSI (57)	Communication Systems (CMS) (61)	Computer Networks and Information Security (CN&IS) (63)
25.04.2016 MON	Advanced Computer Architecture 14MT20501	Operation and Control of Power Systems 14MT20701	Display Technologies and Devices 14MT23801	Physical Design Automation 14MT25701	Adaptive Signal Processing 14MT26101	Ad-hoc Wireless Networks 14MT26301
26.04.2016 TUE	Advanced Operating Systems 14MT20502	Flexible AC Transmission System 14MT20702	Information Theory and Coding Techniques 14MT23802	Low Power VLSI Design 14MT25702	Information Theory and Coding Techniques 14MT23802	Computer Forensics 14MT26302
27.04.2016 WED	Data Warehousing and Data Mining 14MT20503	Power Quality 14MT20703	Microcontroller Based System Design 14MT23803	Mixed Signal Design 14MT25703	Detection and Estimation of Signals 14MT23806	Intrusion Detection Systems 14MT26303
28.04.2016 THU	Object Oriented Analysis and Design 14MT20504	Smart Grid Technology 14MT20704	Testing and Testability of Digital Systems 14MT23804	RF IC Design 14MT25704	Software Defined Radio 14MT26102	Virtualization and Cloud Computing 14MT20509
29.04.2016 FRI	Web Programming 14MT20505	Intelligent Control 14MT20705	Wireless Communications 14MT23805	Testing and Testability 14MT25705	Wireless Communications 14MT23805	Web Programming 14MT20505
30.04.2016 SAT	Big Data Analytics 14MT20506	Energy Audit, Conservation and Management 14MT20709	Optical Communications and Networks 14MT23808	ASIC Design 14MT25706	Radar Signal Processing 14MT26104	TCP/IP Protocols 14MT26306

Note: 1. Students have to bring their college Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
2. Examination will be on the units- I & II.

Copy to: 1) The Director, SVEC

2) The Vice Principal, SVEC

2) The HODs of EEE, ECE, CSE & IT with a request to circulate among the students and staff and display on the Department notice board.

3) The Examination Section

4) S.A.O, for necessary transport arrangement.


PRINCIPAL