

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet – 517 102

M.Tech II Semester (SVEC 14) Mid-II Examinations will be conducted as per the schedule given below.

## TIME-TABLE

Time: 02:30 PM to 04:30 PM

18.07.2016

DATE /DAY	Computer Science (CS) (05)	Electrical Power Systems (EPS) (07)	Digital Electronics and Communication Systems (DECS) (38)	VLSI (57)	Communication Systems (CMS) (61)	Computer Networks and Information Security (CN&IS) (63)
<b>01.08.2016 MON</b>	Advanced Computer Architecture <b>14MT20501</b>	Operation and Control of Power Systems <b>14MT20701</b>	Display Technologies and Devices <b>14MT23801</b>	Physical Design Automation <b>14MT25701</b>	Adaptive Signal Processing <b>14MT26101</b>	Ad-hoc Wireless Networks <b>14MT26301</b>
<b>02.08.2016 TUE</b>	Advanced Operating Systems <b>14MT20502</b>	Flexible AC Transmission System <b>14MT20702</b>	Information Theory and Coding Techniques <b>14MT23802</b>	Low Power VLSI Design <b>14MT25702</b>	Information Theory and Coding Techniques <b>14MT23802</b>	Computer Forensics <b>14MT26302</b>
<b>03.08.2016 WED</b>	Data Warehousing and Data Mining <b>14MT20503</b>	Power Quality <b>14MT20703</b>	Microcontroller Based System Design <b>14MT23803</b>	Mixed Signal Design <b>14MT25703</b>	Detection and Estimation of Signals <b>14MT23806</b>	Intrusion Detection Systems <b>14MT26303</b>
<b>04.08.2016 THU</b>	Object Oriented Analysis and Design <b>14MT20504</b>	Smart Grid Technology <b>14MT20704</b>	Testing and Testability of Digital Systems <b>14MT23804</b>	RF IC Design <b>14MT25704</b>	Software Defined Radio <b>14MT26102</b>	Virtualization and Cloud Computing <b>14MT20509</b>
<b>05.08.2016 FRI</b>	Web Programming <b>14MT20505</b>	Intelligent Control <b>14MT20705</b>	Wireless Communications <b>14MT23805</b>	Testing and Testability <b>14MT25705</b>	Wireless Communications <b>14MT23805</b>	Web Programming <b>14MT20505</b>
<b>08.08.2016 MON</b>	Big Data Analytics <b>14MT20506</b>	Energy Audit, Conservation and Management <b>14MT20709</b>	Optical Communications and Networks <b>14MT23808</b>	ASIC Design <b>14MT25706</b>	Radar Signal Processing <b>14MT26104</b>	TCP/IP Protocols <b>14MT26306</b>

Note: 1.Students have to bring their college Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.

2. Examination will be on the units- III, IV & V.

Copy to: 1) The Director, SVEC.

2) The Vice Principal ,SVEC.

3) The HODs of EEE, ECE & CSE with a request to circulate among the students and staff and display on the Department notice board.

4) The Examination Section

5) S.A.O, for necessary transport arrangement.

  
**PRINCIPAL**