

SREE VIDYANIKETHAN ENGINEERING COLLEGE

SREE SAINATH NAGAR, A. RANGAMPET - 517 102

2nd SHIFT POLYTECHNIC


SVEC/EC/2016-17/508

09.09.2016

CIRCULAR

The V Semester (C14) Diploma Students are informed that the Unit Test-II Examinations will be conducted as per the following schedule.

DAY/ DATE	TIME	DCE	DEEE	DME	DECE	DCME
		SUBJECTS	SUBJECTS	SUBJECTS	SUBJECTS	SUBJECTS
24.09.2016 SATURDAY	03.00 PM To 04.00 PM	Design & Detailing of R.C. Elements C-501	Electrical Utilization EE-501	Industrial Management M-501	Advanced Communications EC-501	JAVA Programming CM - 501
26.09.2016 MONDAY	03.00 PM To 04.00 PM	Environmental Engineering - I C-502	A.C.Machines - II EE-502	Design of Machine Elements -II M-502	Consumer Electronics EC-502	Software Engineering CM - 502
27.09. 2016 TUESDAY	03.00 PM To 04.00 PM	Quantity Surveying - II C-503	Power Systems - II (T&D) EE-503	Estimating & Costing M-503	Computer Hardware EC-503	Advanced Database Systems CM- 503
28.09.2016 WEDNESDAY	03.00 PM To 04.00 PM	Irrigation Engineering C-504	Industrial Drives EE-504	Heat Power Engineering-II M-504	Optical Fibre Communication EC-504	Web Designing CM - 504
29.09.2016 THURSDAY	03.00 PM To 04.00 PM	Project Management for Construction C-505	Digital Electronics EE-505	Fluid Power Control Systems M-505	Microcontroller Applications EC-505	Data Communication CM - 505
30.09.2016 FRIDAY	03.00 PM To 04.00 PM	-----	Maintenance of Electrical Systems EE-506	Production Technology-III M-506	Data communication & Computer Networking EC-506	Cloud Computing CM - 506


PRINCIPAL

- Copy to: 1) The Director, SVEC.
2) The Vice Principal, 2nd Shift Polytechnic: With a request to arrange to read out in the respective classes and to display on the Notice Board.
3) S.A.O. for Information.
4) The Exam Section.
5) Notice Board.