

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet - 517 102

M. Tech I Semester (SVEC16) Mid-I Examinations will be conducted as per the schedule given below.

TIME-TABLE

Time: 02:30 PM to 04:30 PM

05.10.2016

DATE /DAY	Computer Science (CS) (05)	Electrical Power Systems (EPS) (07)	Software Engineering(SE) (25)	Digital Electronics and Communication Systems (DECS) (38)	VLSI (57)	Communication Systems (CMS) (61)	Computer Networks and Information Security (CN&IS) (63)
24.10.2016 MONDAY	ADVANCED COMPUTER NETWORKS (16MT10501)	ADVANCED CONTROL SYSTEMS (16MT10701)	CLOUD COMPUTING (16MT12501)	COMPUTER NETWORKS (16MT13801)	ANALOG IC DESIGN (16MT15701)	ADVANCED DIGITAL SIGNAL PROCESSING (16MT15706)	ADVANCED COMPUTER NETWORKS (16MT10501)
25.10.2016 TUESDAY	ADVANCED DATABASE MANAGEMENT SYSTEMS (16MT10502)	HIGH VOLTAGE ENGINEERING (16MT10702)	SOFTWARE DEVELOPMENT METHODOLOGIES (16MT12503)	DIGITAL COMMUNICATION TECHNIQUES (16MT13802)	COMPUTATIONAL METHODS IN MICROELECTRONICS (16MT15702)	DIGITAL COMMUNICATION TECHNIQUES (16MT13802)	INFORMATION SECURITY (16MT20505)
26.10.2016 WEDNESDAY	ADVANCED OPERATING SYSTEMS (16MT10503)	POWER ELECTRONIC CONVERTERS (16MT10703)	SOFTWARE MEASUREMENT AND METRIES (16MT12504)	DIGITAL SYSTEM DESIGN AND TESTING (16MT13803)	DEVICE MODELING (16MT15703)	INFORMATION THEORY AND CODING TECHNIQUES (16MT23803)	MOBILE COMPUTING (16MT20506)
27.10.2016 THURSDAY	DATA WAREHOUSING AND DATA MINING (16MT10504)	POWER SYSTEM SECURITY AND STATE ESTIMATION (16MT10704)	SOFTWARE REQUIREMENTS AND ESTIMATION (16MT12505)	IMAGE & VIDEO PROCESSING (16MT13804)	DIGITAL IC DESIGN (16MT15704)	OPTICAL COMMUNICATIONS AND NETWORKS (16MT23807)	DATA WAREHOUSING AND DATA MINING (16MT10504)
28.10.2016 FRIDAY	DATA STRUCTURES AND ALGORITHMS (16MT12502)	REACTIVE POWER COMPENSATION AND MANAGEMENT (16MT10705)	DATA STRUCTURES AND ALGORITHMS (16MT12502)	MODERN DIGITAL SIGNAL PROCESSING (16MT13805)	IC FABRICATION (16MT15705)	RF CIRCUIT DESIGN (16MT16101)	WEB TECHNOLOGIES (16MT22505)
29.10.2016 SATURDAY	SOFTWARE TESTING TECHINQUES (16MT22504)	POWER SYSTEM RELIABILITY (16MT10708)	USER INTERFACE DESIGN (16MT12509)	ASIC DESIGN (16MT13806)	REAL TIME SYSTEMS (16MT23808)	DIGITAL SATELLITE COMMUNICATIONS (16MT16102)	INTERNET OF THINGS (16MT10507)

Note: 1.Students have to bring their college Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.

2. Examination will be on the units prescribed in the syllabus Book.

Copy to: 1) The Director, SVEC.

2) The HODs of EEE, ECE, CSE & IT: with a request to circulate among the students and staff and display on the department notice board.

3) The Examination Section

4) The S.A.O, for information.

