



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

Date: 24.10.2016

M.C.A III Semester (SVEC 14) Regular & Supplementary Examinations December, 2016

EXAMINATION SCHEDULE

Time: 02.00 PM To 05.00PM

Date/Day	For 2014 & 2015 Admitted Batches only
13.12.2016 Tuesday	Organizational Behavior and Human Resource Management 14MC3HS01
15.12.2016 Thursday	Operations Research 14MC3BS01
17.12.2016 Saturday	Object Oriented Analysis and Design 14MC30101
20.12.2016 Tuesday	Data Warehousing and Data Mining 14MC30102
22.12.2016 Thursday	Computer Networks 14MC30103

Note: Laboratory examinations will be conducted from 05.12.2016 to 06.12.2016.

CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director. SVEC.

The HOD, MCA : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The S.A.O, SVEC.

SVEC Canteen.

Estate Manager.

Hostel Notice Boards.