Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh II B.Tech I Semester (SVEC-14) Mid-2 Examinations, November 2016

<u>TIME - TABLE</u>

Date: 24/10/2016

Time: 02:45 PM TO 04:45 PM

EXAM DATE								Time : 02.45	PM 10 04:45 PM
& TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	втесн-ме
07/11/2016 2:45PM TO	MONDAY	MATRICES AND NUMERICAL METHODS	PROBABILITY & STATISTICS	PROBABILITY & STATISTICS	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS	PROBABILITY & STATISTICS	MATRICES AND NUMERICAL METHODS
4:45PM		[14BT3BS01]	[14BT3BS03]	[14BT3BS03]	[14BT3BS02]	[14BT3BS02]	[14BT3BS02]	[14BT3BS03]	[14BT3BS01]
08/11/2016 2:45PM TO	TUESDAY	ENGINEERING GEOLOGY	DATA STRUCTURES		SEMICONDUCTOR DEVICES AND CIRCUITS	SEMICONDUCTOR DEVICES AND CIRCUITS	SEMICONDUCTOR DEVICES AND CIRCUITS	DATA STRUCTURES	BASICS OF ELECTRICAL AND ELECTRONICS ENGINEERING
4:45PM		[14BT30101]	[14BT30501]	[14BT30501]	[14BT30402]	[14BT30402]	[14BT30402]	[14BT30501]	[14BT30234]
09/11/2016 2:45PM TO	WEDNESDAY	BASICS OF ELECTRICAL AND MECHANICAL TECHNOLOGY	BASIC ELECTRICAL ENGINEERING	FOUNDATIONS OF ELECTRICAL ENGINEERING	ENVIRONMENTAL SCIENCES	ENVIRONMENTAL SCIENCES	ENVIRONMENTAL SCIENCES	BASIC ELECTRICAL ENGINEERING	STRENGTH OF MATERIALS
4:45PM		[14BT30233]	[14BT30235]	[14BT30236]	[14BT3HS01]	[14BT3HS01]	[14BT3HS01]	[14BT30235]	[14BT30301]
10/11/2016 2:45PM TO	THURSDAY	MECHANICS OF SOILDS	DIGITAL LOGIC DESIGN	DIGITAL LOGIC DESIGN	ELECTRICAL TECHNOLOGY	ELECTROMAGNETIC FIELDS	PRINCIPLES OF ELECTRICAL MEASURMENTS	DIGITAL LOGIC DESIGN	MATERIALS SCIENCE AND METALLURGY
4:45PM		[14BT30102]	[14BT30502]	[14BT30502]	[14BT30231]	[14BT30201]	[14BT31001]	[14BT30502]	[14BT30302]
11/11/2016 2:45PM TO	FRIDAY	BUILDING MATERIALS AND CONCRETE TECHNOLOGY	DISCRETE MATHEMATICAL STRUCTURES	DISCRETE MATHEMATICAL STRUCTURES	PROBABILITY AND STOCHASTIC PROCESSES	DC MACHINES	SENSORS AND TRANSDUCERS	DISCRETE MATHEMATICAL STRUCTURES	THERMODYNAMICS
4:45PM		[14BT30103]	[14BT31201]	[14BT31201]	[14BT30401]	[14BT30202]	[14BT31002]	[14BT31201]	[14BT30303]
12/11/2016 2:45PM TO	SATURDAY	FLUID MECHANICS I	ELECTRONIC DEVICES AND CIRCUITS	ELECTRONIC DEVICES AND CIRCUITS	NETWORK ANALYSIS	ELECTRIC CIRCUITS	NETWORK ANALYSIS	ELECTRONIC DEVICES AND CIRCUITS	MANUFACTURING TECHNOLOGY-I
4:45PM		[14BT30104]	[14BT30431]	[14BT30431]	[14BT30232]	[14BT30203]	[14BT30232]	[14BT30431]	[14BT30304]

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.

2. Examination will be on the Units prescribed in the Syllabus Book; 3. College buses will leave the college with in 15 minutes after Examination Time.

Copy to: 1) The Director, SVEC.

2) The HODs of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & BS&H: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board; 3) The Examination Section.

4) The S.A.O, for information and necessary transport arrangements.