



# SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

Date: 31.10.2016

## M.C.A V Semester (SVEC 14) Regular Examinations December, 2016.

### EXAMINATION SCHEDULE

Time: 10.00 AM To 01.00PM

Date/Day	For 2014 Admitted Batch only
19.12.2016 Monday	INFORMATION SECURITY 14MC50101
21.12.2016 Wednesday	SOFTWARE TESTING 14MC50102
23.12.2016 Friday	CLOUD COMPUTING 14MC50103
26.12.2016 Monday	MIDDLEWARE TECHNOLOGY 14MC50106
28.12.2016 Wednesday	BUSINESS INTELLIGENCE 14MC50108

Note: Laboratory examinations will be conducted from 13.12.2016 to 14.12.2016.

  
CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director. SVEC.

The HOD, MCA : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The SA.O, S.V.E.C.

Hostel Notice Boards.