



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

Date: 31.10.2016

M.C.A V Semester (SVEC 10) Supplementary Examinations **December, 2016.**

EXAMINATION SCHEDULE

Time: 10.00 AM To 01.00PM

| Date/Day | For 2011,2012 & 2013 Admitted Batches only |
|-------------------------|--|
| 19.12.2016 Monday | OBJECT ORIENTED ANALYSIS AND DESIGN 10MC50101 |
| 21.12.2016 Wednesday | MANAGEMENT INFORMATION SYSTEMS 10MC50102 |
| 23.12.2016 Friday | MIDDLEWARE TECHNOLOGIES 10MC50103 |
| 26.12.2016 Monday | SOFTWARE TESTING 10MC50106 |
| 28.12.2016 Wednesday | CLOUD COMPUTING 10MC50108 |

Note: Laboratory examinations will be conducted from 13.12.2016 to 14.12.2016.


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director. SVEC.

The HOD, MCA : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The S.A.O, SVEC.

The Manager, Andhra Bank, Sri Vidyanikethan Branch.

Hostel Notice Boards.