

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)
Sree Sainath Nagar, Tirupati- 517 102

Date: 30.11.2016

M.C.A I Semester (SVEC 16) Regular Examinations January, 2017

EXAMINATION SCHEDULE

Time: 02.00 PM To 05.00PM

Date/Day	For 2016 Admitted Batch only
23.01.2017 Monday	Accounting And Financial Management 16MC1HS01
25.01.2017 Wednesday	Mathematical Foundations Of Computer Science 16MC1BS01
27.01.2017 Friday	Computer Organization 16MC10101
30.01.2017 Monday	Operating Systems 16MC10102
01.02.2017 Wednesday	Programming In C 16MC10103

Note: Laboratory examinations will be conducted from 09.01.2017 to 10.01.2017.

CHIEF CONTROLLER OF EXAMINATIONS

Copy to: The Director. SVEC.

The HOD, MCA: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The S.A.O, SVEC. SVEC Canteen.

Estate Manager.

Hostel Notice Boards.