

# SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, Tirupati- 517 102

Date: 30.11.2016

**M.C.A I Semester (SVEC14) Supplementary Examinations**  
**January, 2017**

**EXAMINATION SCHEDULE**

**Time: 02.00 PM To 05.00 PM**

Date/Day	For 2014 & 2015 Admitted Batches only
23.01.2017 Monday	ACCOUNTING AND FINANCIAL MANAGEMENT 14MC1HS01
25.01.2017 Wednesday	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE 14MC1BS01
27.01.2017 Friday	PROGRAMMING THROUGH C 14MC10101
30.01.2017 Monday	COMPUTER ORGANIZATION 14MC10102
01.02.2017 Wednesday	DATA STRUCTURES 14MC10103
03.02.2017 Friday	PROFESSIONAL COMMUNICATION 14MC1HS02

Note: Laboratory examinations will be conducted from 09.01.2017 to 10.01.2017

  
**CHIEF CONTROLLER OF EXAMINATIONS**

Copy to : The Director. SVEC.

The HOD, MCA : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The S.A.O, SVEC.

SVEC Canteen.

Estate Manager.

Hostel Notice Boards.