SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati– 517 102
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Ananthapuramu)

M.Tech I Semester (SVEC-14) Supplementary Examinations January, 2017

EXAMINATION SCHEDULE

Date: 30.11.2016

Time: 02.00 PM To 05.00 PM

DATE /DAY	Computer Science (CS) (05)	Electrical Power Systems (EPS) (07)	Software Engineering(SE) (25)	Digital Electronics and Communication Systems (DECS) (38)	VLSI (57)	Communication Systems (CMS) (61)	Computer Networks and Information Security (CN&IS) (63)
			For 2014 & 2015 Admitted Batches only				
23.01.2017 Monday	ADVANCED COMPUTER NETWORKS (14MT10501)	POWER SYSTEM SECURITY AND STATE ESTIMATION (14MT10701)	SOFTWARE DEVELOPMENT METHODOLOGIES (14MT12501)	COMPUTER ARCHITECTURES (14MT13801)	ANALOG IC DESIGN (14MT15701)	ADVANCED DIGITAL SIGNAL PROCESSING (14MT15706)	ADVANCED COMPUTER NETWORKS (14MT10501)
25.01.2017 Wednesday	ADVANCED DATABASE MANAGEMENT SYSTEMS (14MT10502)	STATIC AND DIGITAL PROTECTION OF POWER SYSTEM (14MT10702)	SOFTWARE PROCESS AND PROJECT MANAGEMENT (14MT12502)	DIGITAL COMMUNICATION TECHNIQUES (14MT13802)	COMPUTATIONAL TECHNIQUES IN MICROELECTRONICS (14MT15702)	DIGITAL COMMUNICATION TECHNIQUES (14MT13802)	INFORMATION SECURITY (14MT20507)
28.01.2017 Saturday	ADVANCED SOFTWARE ENGINEERING (14MT10503)	ADVANCED POWER SYSTEM STABILITY ANALYSIS (14MT10703)	SOFTWARE REQUIREMENTS AND ESTIMATION (14MT12503)	DIGITAL SYSTEM DESIGN (14MT13803)	DEVICE MODELING (14MT15703)	COMPUTER NETWORKS (14MT13805)	MOBILE COMPUTING (14MT20508)
31.01.2017 Tuesday	DATA STRUCTURES AND ALGORITHMS (14MT10504)	POWER ELECTRONIC CONVERTERS (14MT10704)	DATA STRUCTURES AND ALGORITHMS (14MT10504)	LINEAR ALGEBRA (14MT13809)	DIGITAL IC DESIGN (14MT5704)	LINEAR ALGEBRA (14MT13809)	NETWORK MANAGEMENT (14MT16301)
02.02.2017 Thursday	DISCRETE STRUCTURES AND GRAPH THEORY (14MT10505)	ADVANCED CONTROL SYSTEMS (14MT10705)	WEB TECHNOLOGIES (14MT12504)	MODERN DIGITAL SIGNAL PROCESSING (14MT13804)	IC FABRICATION (14MT15705)	OPTICAL COMMUNICATIONS AND NETWORKS (14MT23808)	WIRELESS NETWORKS (14MT16302)
06.02.2017 Monday	SOFTWARE PROJECT MANAGEMENT (14MT10509)	REACTIVE POWER COMPENSATION AND MANAGEMENT (14MT10707)	CLOUD COMPUTING (14MT12508)	EMBEDDED SYSTEM DESIGN (14MT13807)	LOW VOLTAGE ANALOG CIRCUIT DESIGN (14MT15708)	SATELLITE COMMUNICATIONS (14MT16102)	DATA WAREHOUSING AND DATA MINING (14MT20503)
08.02.2017 Wednesday	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)	RESEARCH METHODOLOGY (14MT10310)

Copy to : The Director. SVEC.

    The HODs, of EEE,ECE,CSE & IT : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

    The Examination Section.

    The S.A.O, SVEC   SVEC Canteen. , Estate Manager, Hostel Notice Boards.

CHIEF CONTROLLER OF EXAMINATIONS