

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Ananthapuramu)

**M.Tech I Semester (SVEC-16) Regular Examinations January, 2017**

## EXAMINATION SCHEDULE

Date: 30.11.2016

Time: 02.00 PM To 05.00 PM

DATE / DAY	For 2016 Admitted Batch only						
	Computer Science (CS) (05)	Electrical Power Systems (EPS) (07)	Software Engineering(SE) (25)	Digital Electronics and Communication Systems (DECS) (38)	VLSI (57)	Communication Systems (CMS) (61)	Computer Networks and Information Security (CN&IS) (63)
23.01.2017 Monday	ADVANCED COMPUTER NETWORKS (16MT10501)	ADVANCED CONTROL SYSTEMS (16MT10701)	CLOUD COMPUTING (16MT12501)	COMPUTER NETWORKS (16MT13801)	ANALOG IC DESIGN (16MT15701)	ADVANCED DIGITAL SIGNAL PROCESSING (16MT15706)	ADVANCED COMPUTER NETWORKS (16MT10501)
25.01.2017 Wednesday	ADVANCED DATABASE MANAGEMENT SYSTEMS (16MT10502)	HIGH VOLTAGE ENGINEERING (16MT10702)	SOFTWARE DEVELOPMENT METHODOLOGIES (16MT12503)	DIGITAL COMMUNICATION TECHNIQUES (16MT13802)	COMPUTATIONAL METHODS IN MICROELECTRONICS (16MT15702)	DIGITAL COMMUNICATION TECHNIQUES (16MT13802)	INFORMATION SECURITY (16MT20505)
28.01.2017 Saturday	ADVANCED OPERATING SYSTEMS (16MT10503)	POWER ELECTRONIC CONVERTERS (16MT10703)	SOFTWARE MEASUREMENT AND METRIES (16MT12504)	DIGITAL SYSTEM DESIGN AND TESTING (16MT13803)	DEVICE MODELING (16MT15703)	INFORMATION THEORY AND CODING TECHNIQUES (16MT23803)	MOBILE COMPUTING (16MT20506)
31.01.2017 Tuesday	DATA WAREHOUSING AND DATA MINING (16MT10504)	POWER SYSTEM SECURITY AND STATE ESTIMATION (16MT10704)	SOFTWARE REQUIREMENTS AND ESTIMATION (16MT12505)	IMAGE & VIDEO PROCESSING (16MT13804)	DIGITAL IC DESIGN (16MT15704)	OPTICAL COMMUNICATIONS AND NETWORKS (16MT23807)	DATA WAREHOUSING AND DATA MINING (16MT10504)
02.02.2017 Thursday	DATA STRUCTURES AND ALGORITHMS (16MT12502)	REACTIVE POWER COMPENSATION AND MANAGEMENT (16MT10705)	DATA STRUCTURES AND ALGORITHMS (16MT12502)	MODERN DIGITAL SIGNAL PROCESSING (16MT13805)	IC FABRICATION (16MT15705)	RF CIRCUIT DESIGN (16MT16101)	WEB TECHNOLOGIES (16MT22505)
06.02.2017 Monday	SOFTWARE TESTING TECHNIQUES (16MT22504)	POWER SYSTEM RELIABILITY (16MT10708)	USER INTERFACE DESIGN (16MT12509)	ASIC DESIGN (16MT13806)	REAL TIME SYSTEMS (16MT23808)	DIGITAL SATELLITE COMMUNICATIONS (16MT16102)	INTERNET OF THINGS (16MT10507)

**CHIEF CONTROLLER OF EXAMINATIONS**

Copy to : The Director. SVEC.

The HODs, of EEE,ECE,CSE & IT : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The S.A.O, SVEC SVEC Canteen. , Estate Manager, Hostel Notice Boards.