

EXAMINATION SCHEDULE

Date: 30.11.2016

Time: 02.00 PM To 05.00 PM

DATE / DAY	For 2014 & 2015 Admitted Batches only						
	Computer Science (CS) (05)	Electrical Power Systems (EPS) (07)	Software Engineering(SE) (25)	Digital Electronics and Communication Systems (DECS) (38)	VLSI (57)	Communication Systems (CMS) (61)	Computer Networks and Information Security (CN&IS) (63)
24.01.2017 Tuesday	Advanced Computer Architecture 14MT20501	Operation and Control of Power Systems 14MT20701	Service Oriented Architecture 14MT22501	Display Technologies and Devices 14MT23801	Physical Design Automation 14MT25701	Adaptive Signal Processing 14MT26101	Ad-hoc Wireless Networks 14MT26301
27.01.2017 Friday	Advanced Operating Systems 14MT20502	Flexible AC Transmission System 14MT20702	Software Architecture & Design Patterns 14MT22502	Information Theory and Coding Techniques 14MT23802	Low Power VLSI Design 14MT25702	Information Theory and Coding Techniques 14MT23802	Computer Forensics 14MT26302
30.01.2017 Monday	Data Warehousing and Data Mining 14MT20503	Power Quality 14MT20703	Software Measurement and Metrics 14MT22503	Microcontroller Based System Design 14MT23803	Mixed Signal Design 14MT25703	Detection and Estimation of Signals 14MT23806	Intrusion Detection Systems 14MT26303
01.02.2017 Wednesday	Object Oriented Analysis and Design 14MT20504	Smart Grid Technology 14MT20704	Software Security Engineering 14MT22504	Testing and Testability of Digital Systems 14MT23804	RF IC Design 14MT25704	Software Defined Radio 14MT26102	Virtualization and Cloud Computing 14MT20509
03.02.2017 Friday	Web Programming 14MT20505	Intelligent Control 14MT20705	Software Testing 14MT22505	Wireless Communications 14MT23805	Testing and Testability 14MT25705	Wireless Communications 14MT23805	Web Programming 14MT20505
07.02.2017 Tuesday	Mobile Computing 14MT20508	Power System Reliability 14MT20708	Big Data Technologies 14MT22506	Optical Communications and Networks 14MT23808	Co-Design 14MT25707	Radar Signal Processing 14MT26104	TCP/IP Protocols 14MT26306
	Big Data Analytics 14MT20506	Energy Audit, Conservation and Management 14MT20709			ASIC Design 14MT25706		

P. C. Chao

CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director. SVEC.

The HODs, of EEE, ECE, CSE & IT : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The S.A.O, SVEC SVEC Canteen. , Estate Manager, Hostel Notice Boards.