Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

M.Tech I Semester (SVEC 16) Mid II Internal Examinations, January-2017

TIME - TABLE

Date : 19/12/2016

Time : 02:30 PM TO 04:30 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CMS	MTECH-CNIS	MTECH-CS	MTECH-DECS	MTECH-EPS	MTECH-SE	MTECH-VLSI
02/01/2017 2:30PM TO 4:30PM	MONDAY	ADVANCED DIGITAL SIGNAL PROCESSING [16MT15706]	ADVANCED COMPUTER NETWORKS [16MT10501]	ADVANCED COMPUTER NETWORKS [16MT10501]	COMPUTER NETWORKS [16MT13801]	ADVANCED CONTROL SYSTEMS [16MT10701]	CLOUD COMPUTING [16MT12501]	ANALOG IC DESIGN [16MT15701]
03/01/2017 2:30PM TO 4:30PM	TUESDAY	DIGITAL COMMUNICATION TECHNIQUES [16MT13802]	INFORMATION SECURITY [16MT20505]	ADVANCED DATABASE MANAGEMENT SYSTEMS [16MT10502]	DIGITAL COMMUNICATION TECHNIQUES [16MT13802]	HIGH VOLTAGE ENGINEERING [16MT10702]	SOFTWARE DEVELOPMENT METHODOLOGIES [16MT12503]	COMPUTATIONAL METHODS IN MICROELECTRONICS [16MT15702]
04/01/2017 2:30PM TO 4:30PM	WEDNESDAY	INFORMATION THEORY AND CODING TECHNIQUES [16MT23803]	MOBILE COMPUTING [16MT20506]	ADVANCED OPERATING SYSTEMS [16MT10503]	DIGITAL SYSTEM DESIGN AND TESTING [16MT13803]	POWER ELECTRONIC CONVERTERS [16MT10703]	SOFTWARE MEASUREMENT AND METRICS [16MT12504]	DEVICE MODELING [16MT15703]
05/01/2017 2:30PM TO 4:30PM	THURSDAY	OPTICAL COMMUNICATIONS AND NETWORKS [16MT23807]	DATA WAREHOUSING AND DATA MINING [16MT10504]	DATA WAREHOUSING AND DATA MINING [16MT10504]	IMAGE & VIDEO PROCESSING [16MT13804]	POWER SYSTEM SECURITY AND STATE ESTIMATION [16MT10704]	SOFTWARE REQUIREMENTS AND ESTIMATION [16MT12505]	DIGITAL IC DESIGN [16MT15704]
06/01/2017 2:30PM TO 4:30PM	FRIDAY	RF CIRCUIT DESIGN [16MT16101]	WEB TECHNOLOGIES [16MT22505]	DATA STRUCTURES AND ALGORITHMS [16MT12502]	MODERN DIGITAL SIGNAL PROCESSING [16MT13805]	REACTIVE POWER COMPENSATION AND MANAGEMENT [16MT10705]	DATA STRUCTURES AND ALGORITHMS [16MT12502]	IC FABRICATION [16MT15705]
09/01/2017 2:30PM TO 4:30PM	MONDAY	DIGITAL SATELLITE COMMUNICATIONS [16MT16102]	INTERNET OF THINGS [16MT10507]	SOFTWARE TESTING TECHNIQUES [16MT22504]	ASIC DESIGN [16MT13806]	POWER SYSTEM RELIABILITY [16MT10708]	USER INTERFACE DESIGN [16MT12509]	REAL TIME SYSTEMS [16MT23808]

Note: 1. Students have to bring their college Identity Cards to the examination hall ; otherwise they will not be permitted to write the examination.

2. Examination will be on the units prescribed in the syllabus Book.

PRINCIPAL

Copy to: 1) The Director, SVEC.

2) The HODs of EEE, ECE, CSE & IT : with a request to circulate among the students and staff and display on the department notice board.

3) The Examination Section

4) The S.A.O, for information.