Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh II B.Tech II Semester (SVEC-14) Mid-1 Examinations, February 2017

<u>TIME - TABLE</u>

Date: 06/02/2017

Time: 10:30 AM TO 12:30 PM

		T						Time: 10:30	AM TO 12:30 PM
EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	втесн-ме
20/02/2017 10:30 AM TO 12:30PM	MONDAY	BUSINESS COMMUNICATION AND PRESENTATION SKILLS [14BT4HS01]	BUSINESS COMMUNICATION AND PRESENTATION SKILLS [14BT4HS01]	BUSINESS COMMUNICATION AND PRESENTATION SKILLS [14BT4HS01]	PROFESSIONAL ETHICS [14BT4HS02]	PROFESSIONAL ETHICS	PROFESSIONAL ETHICS	BUSINESS COMMUNICATION AND PRESENTATION SKILLS	BUSINESS COMMUNICATION AND PRESENTATION SKILLS
21/02/2017 10:30AM TO 12:30PM	TUESDAY	ENVIRONMENTAL	ENVIRONMENTAL	ENVIRONMENTAL	ANALOG		[14BT4HS02]	[14BT4HS01]	[14BT4HS01]
		SCIENCES	SCIENCES	SCIENCES	COMMUNICATIONS	SIGNALS AND NETWORKS	ELECTRICAL TECHNOLOGY	ENVIRONMENTAL SCIENCES	ENVIRONMENTAL SCIENCES
		[14BT3HS01]	[14BT3HS01]	[14BT3HS01]	[14BT40401]	[14BT40201]	[14BT30231]	[14BT3HS01]	[14BT3HS01]
22/02/2017 10:30AM TO 12:30PM	WEDNESDAY	PROBABILITY & STATISTICS	COMPUTER ORGANIZATION	COMPUTER ORGANIZATION	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN	GENERATION OF ELECTRIC POWER	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN	COMPUTER ORGANIZATION	PROBABILITY & STATISTICS
		[14BT3BS03]	[14BT40501]	[14BT40501]	[14BT40402]	[14BT40202]	[14BT40402]	[14BT40501]	[14BT3BS03]
23/02/2017 10:30AM TO 12:30PM	THURSDAY	STRUCTURAL ANALYSIS -	DATABASE MANAGEMENT SYSTEMS	DATABASE MANAGEMENT SYSTEMS	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	ELECTRICAL AND ELECTRONIC MEASUREMENTS	PULSE AND DIGITAL CIRCUITS	DATABASE MANAGEMENT SYSTEMS	KINEMATICS OF MACHINERY
12.001 111		[14BT40101]	[14BT40502]	[14BT40502]	[14BT40403]	[14BT40203]	[14BT50405]	[14BT40502]	[14BT40301]
25/02/2017 10:30AM TO 12:30PM	SATURDAY	CONSTRUCTION, PLANNING & PROJECT MANAGEMENT	DESIGN AND ANALYSIS OF ALGORITHMS	THEORY OF COMPUTATION	SIGNALS AND SYSTEMS	TRANSFORMERS AND INDUCTION MACHINES	SIGNALS AND	OPERATING SYSTEMS	FLUID MECHANICS AND HYDRAULIC MACHINERY
		[14BT40102]	[14BT40503]	[14BT50501]	[14BT40404]	[14BT40204]	[14BT40404]	[14BT51501]	[14BT40302]

27/02/2017 10:30AM TO	MONDAY	SURVEYING	OBJECT ORIENTED PROGRAMMING	OBJECT ORIENTED PROGRAMMING	SWITCHING THEORY AND LOGIC DESIGN	SWITCHING THEORY AND LOGIC DESIGN	SWITCHING THEORY AND LOGIC DESIGN	OBJECT ORIENTED PROGRAMMING	THERMAL ENGINEERING-I
12:30PM		[14BT40103]	[14BT41201]	[14BT41201]	[14BT40405]	[14BT40405]	[14BT40405]	[14BT41201]	[14BT40303]
28/02/2017 10:30AM TO	TUESDAY	FLUID MECHANICS - II	COMPUTER GRAPHICS	COMPUTER GRAPHICS	CONTROL SYSTEMS	ANALOG ELECTRONIC CIRCUITS	CONTROL SYSTEMS	DATA COMMUNICATIONS	MANUFACTURING TECHNOLOGY-II
12:30PM		[14BT40104]	[14BT41501]	[14BT41501]	[14BT50201]	[14BT40431]	[14BT50201]	[14BT40432]	[14BT40304]

^{1.} Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.

Copy to: 1) The Director, SVEC.

2) The HODs of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & BS&H: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.

3) The Examination Section.

4) The S.A.O, for information and necessary transport arrangement.

^{2.} Examination will be on the Units prescribed in the Syllabus Book.

^{3.} College buses will leave the college with in 15 minutes after Examination Time.