

SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

II B.Tech II Semester (SVEC-14) Regular/Supplementary Examinations May, 2017

EXAMINATION SCHEDULE

Date: 03-04-2017

Time: 10.00 AM To 01.00PM

DATE /DAY	For 2014, 2015 Regular and 2015, 2016 Lateral Entry Admitted Batches only							
	CE	EEE	ME	ECE	CSE	EIE	IT	CSSE
29/05/2017 MONDAY	BUSINESS COMMUNICATION AND PRESENTATION SKILLS 14BT4HS01	PROFESSIONAL ETHICS 14BT4HS02	BUSINESS COMMUNICATION AND PRESENTATION SKILLS 14BT4HS01	PROFESSIONAL ETHICS 14BT4HS02	BUSINESS COMMUNICATION AND PRESENTATION SKILLS 14BT4HS01	PROFESSIONAL ETHICS 14BT4HS02	BUSINESS COMMUNICATION AND PRESENTATION SKILLS 14BT4HS01	BUSINESS COMMUNICATION AND PRESENTATION SKILLS 14BT4HS01
31/05/2017 WEDNESDAY	ENVIRONMENTAL SCIENCES 14BT3HS01	SIGNALS AND NETWORKS 14BT40201	ENVIRONMENTAL SCIENCES 14BT3HS01	ANALOG COMMUNICATIONS 14BT40401	ENVIRONMENTAL SCIENCES 14BT3HS01	ELECTRICAL TECHNOLOGY 14BT30231	ENVIRONMENTAL SCIENCES 14BT3HS01	ENVIRONMENTAL SCIENCES 14BT3HS01
02/06/2017 FRIDAY	PROBABILITY & STATISTICS 14BT3BS03	GENERATION OF ELECTRICAL POWER 14BT40202	PROBABILITY & STATISTICS 14BT3BS03	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN 14BT40402	COMPUTER ORGANIZATION 14BT40501	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN 14BT40402	COMPUTER ORGANIZATION 14BT40501	COMPUTER ORGANIZATION 14BT40501
06/06/2017 TUESDAY	STRUCTURAL ANALYSIS - I 14BT40101	ELECTRICAL AND ELECTRONICS MEASUREMENTS 14BT40203	KINEMATICS OF MACHINERY 14BT40301	ELECTRO MAGNETIC THEORY AND TRANSMISSION LINES 14BT40403	DATA BASE MANAGEMENT SYSTEMS 14BT40502	PULSE AND DIGITAL CIRCUITS 14BT50405	DATA BASE MANAGEMENT SYSTEMS 14BT40502	DATA BASE MANAGEMENT SYSTEMS 14BT40502
08/06/2017 THURSDAY	CONSTRUCTION, PLANNING AND PROJECT MANAGEMENT 14BT40102	TRANSFORMERS AND INDUCTION MACHINES 14BT40204	FLUID MECHANICS AND HYDRAULIC MACHINERY 14BT40302	SIGNALS AND SYSTEMS 14BT40404	DESIGN AND ANALYSIS OF ALGORITHMS 14BT40503	SIGNALS AND SYSTEMS 14BT40404	OPERATING SYSTEMS 14BT51501	THEORY OF COMPUTATION 14BT50501
10/06/2017 SATURDAY	SURVEYING 14BT40103	SWITCHING THEORY AND LOGIC DESIGN 14BT40405	THERMAL ENGINEERING - I 14BT40303	SWITCHING THEORY AND LOGIC DESIGN 14BT40405	OBJECT ORIENTED PROGRAMMING 14BT41201	SWITCHING THEORY AND LOGIC DESIGN 14BT40405	OBJECT ORIENTED PROGRAMMING 14BT41201	OBJECT ORIENTED PROGRAMMING 14BT41201
13/06/2017 TUESDAY	FLUID MECHANICS- II 14BT40104	ANALOG ELECTRONIC CIRCUITS 14BT40431	MANUFACTURING TECHNOLOGY-II 14BT40304	CONTROL SYSTEMS 14BT50201	COMPUTER GRAPHICS 14BT41501	CONTROL SYSTEMS 14BT50201	DATA COMMUNICATIONS 14BT40432	COMPUTER GRAPHICS 14BT41501


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT & CSSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.