

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, Tirupati – 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

## I B.Tech II Semester (SVEC-16) Regular Examinations June, 2017

### EXAMINATION SCHEDULE

Date: 27-04-2017

Time: 10:00 AM To 01:00 PM

EXAM DATE & TIME	For 2016 Admitted Batch only								
	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
19/06/2017	MONDAY	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]	TECHNICAL ENGLISH [16BT1HS01]	TECHNICAL ENGLISH [16BT1HS01]	TECHNICAL ENGLISH [16BT1HS01]	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]
21/06/2017	WEDNESDAY	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]
23/06/2017	FRIDAY	BUILDING MATERIAL AND CONSTRUCTION TECHNOLOGY [16BT20101]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	ENGINEERING MATERIALS [16BT20301]
27/06/2017	TUESDAY	ENGINEERING MECHANICS [16BT20102]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	ENGINEERING CHEMISTRY [16BT1BS01]	ENGINEERING CHEMISTRY [16BT1BS01]	ENGINEERING CHEMISTRY [16BT1BS01]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	ENGINEERING MECHANICS [16BT20102]
29/06/2017	THURSDAY	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241]	DIGITAL LOGIC DESIGN [16BT21501]	DIGITAL LOGIC DESIGN [16BT21501]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	DIGITAL LOGIC DESIGN [16BT21501]	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241]

  
**CHIEF CONTROLLER OF EXAMINATIONS**

Copy to: The Director, SVEC.

The HODs, of CE, M.E, EEE, ECE, CSE, EIE, IT, CSSE & BS&H: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The Examination Section; The S.A.O, SVEC; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.