

**Sree Vidyanikethan Engineering College (Autonomous)**  
 Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh  
**M.Tech II Semester (SVEC-14) Supplementary Examinations, August 2017**  
TIME - TABLE

Date : 24/06/2017

Time : 10:00 AM TO 01:00 PM

| EXAM DATE & TIME                | WEEK DAY  | MTECH-CMS   | MTECH-CNIS  | MTECH-CS  | MTECH-DECS  | MTECH-EPS   | MTECH-VLSI  |
|---------------------------------|-----------|---|---|---|---|---|---|
| 16/08/2017<br>10:00AM TO 1:00PM | WEDNESDAY | ADAPTIVE SIGNAL PROCESSING<br>[14MT26101]               | AD-HOC WIRELESS NETWORKS<br>[14MT26301]           | ADVANCED COMPUTER ARCHITECTURE<br>[14MT20501]                         | DISPLAY TECHNOLOGIES AND DEVICES<br>[14MT23801]           | OPERATION AND CONTROL OF POWER SYSTEMS<br>[14MT20701]   | PHYSICAL DESIGN AUTOMATION<br>[14MT25701]               |
| 18/08/2017<br>10:00AM TO 1:00PM | FRIDAY    | INFORMATION THEORY AND CODING TECHNIQUES<br>[14MT23802] | COMPUTER FORENSICS<br>[14MT26302]                 | ADVANCED OPERATING SYSTEMS<br>[14MT20502]                             | INFORMATION THEORY AND CODING TECHNIQUES<br>[14MT23802]   | FLEXIBLE AC TRANSMISSION SYSTEM<br>[14MT20702]  | LOW POWER VLSI DESIGN<br>[14MT25702]                    |
| 21/08/2017<br>10:00AM TO 1:00PM | MONDAY    | DETECTION AND ESTIMATION OF SIGNALS<br>[14MT23806]      | INTRUSION DETECTION SYSTEMS<br>[14MT26303]        | DATA WAREHOUSING AND DATA MINING<br>[14MT20503]                       | MICROCONTROLLER BASED SYSTEM DESIGN<br>[14MT23803]        | POWER QUALITY<br>[14MT20703]  | MIXED SIGNAL DESIGN<br>[14MT25703]                      |
| 23/08/2017<br>10:00AM TO 1:00PM | WEDNESDAY | SOFTWARE DEFINED RADIO<br>[14MT26102]                   | VIRTUALIZATION AND CLOUD COMPUTING<br>[14MT20509] | OBJECT ORIENTED ANALYSIS AND DESIGN<br>[14MT20504]                    | TESTING AND TESTABILITY OF DIGITAL SYSTEMS<br>[14MT23804] | SMART GRID TECHNOLOGY<br>[14MT20704]  | RF IC DESIGN<br>[14MT25704]                             |
| 26/08/2017<br>10:00AM TO 1:00PM | SATURDAY  | WIRELESS COMMUNICATIONS<br>[14MT23805]                  | WEB PROGRAMMING<br>[14MT20505]                    | WEB PROGRAMMING<br>[14MT20505]  | WIRELESS COMMUNICATIONS<br>[14MT23805]                    | INTELLIGENT CONTROL<br>[14MT20705]  | TESTING AND TESTABILITY<br>[14MT25705]                  |
| 29/08/2017<br>10:00AM TO 1:00PM | TUESDAY   | RADAR SIGNAL PROCESSING<br>[14MT26104]                  | TCP/IP PROTOCOLS<br>[14MT26306]                   | BIG DATA ANALYTICS<br>[14MT20506],<br>MOBILE COMPUTING<br>[14MT20508] | OPTICAL COMMUNICATIONS AND NETWORKS<br>[14MT23808]        | ENERGY AUDIT , CONSERVATION AND MANAGEMENT<br>[14MT20709],<br>POWER SYSTEM RELIABILITY<br>[14MT20708] | ASIC DESIGN<br>[14MT25706],<br>CO-DESIGN<br>[14MT25707] |

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
2. Examination will be on the Units prescribed in the Syllabus Book.
3. College buses will leave the college with in 15 minutes after Examination Time.

Copy to: 1) The Director, SVEC.

2) The HODs of EEE, ECE, CSE & IT: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.

3) The Examination Section.

4) The S.A.O, for information and necessary transport arrangement.

  
**PRINCIPAL**