

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

M.Tech II Semester (SVEC 16) Mid-II Examinations, July-2017

TIME - TABLE

Date : 10/07/2017

Time : 02:30 PM TO 04:30 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CMS	MTECH-CNIS	MTECH-CS	MTECH-DECS	MTECH-EPS	MTECH-SE	MTECH-VLSI
24/07/2017 2:30PM TO 4:30PM	MONDAY	DETECTION AND ESTIMATION OF SIGNALS [16MT23801]	INTRUSION DETECTION SYSTEMS [16MT26301]	ADVANCED COMPUTER ARCHITECTURE [16MT20501]	DETECTION AND ESTIMATION OF SIGNALS [16MT23801]	FLEXIBLE AC TRANSMISSION SYSTEM [16MT20701]	BIG DATA TECHNOLOGIES [16MT22501]	LOW POWER VLSI DESIGN [16MT25701]
25/07/2017 2:30PM TO 4:30PM	TUESDAY	ADAPTIVE SIGNAL PROCESSING [16MT26101]	BIG DATA ANALYTICS [16MT20502]	BIG DATA ANALYTICS [16MT20502]	EMBEDDED SYSTEM DESIGN [16MT23802]	INTELLIGENT SYSTEMS [16MT20702]	SERVICE ORIENTED ARCHITECTURE [16MT22502]	MIXED SIGNAL DESIGN [16MT25702]
26/07/2017 2:30PM TO 4:30PM	WEDNESDAY	IMAGE & VIDEO PROCESSING [16MT13804]	NETWORK PROGRAMMING [16MT26302]	OBJECT ORIENTED ANALYSIS AND DESIGN [16MT20503]	INFORMATION THEORY AND CODING TECHNIQUES [16MT23803]	POWER SYSTEM STABILITY AND CONTROL [16MT20703]	SOFTWARE ARCHITECTURE & DESIGN PATTERNS [16MT22503]	NANOELECTRONICS [16MT25703]
27/07/2017 2:30PM TO 4:30PM	THURSDAY	SMART ANTENNAS [16MT26102]	CLOUD COMPUTING [16MT12501]	CLOUD COMPUTING [16MT12501]	LOW POWER CMOS VLSI DESIGN [16MT23804]	RESTRUCTURED POWER SYSTEM [16MT20704]	SOFTWARE TESTING TECHNIQUES [16MT22504]	PHYSICAL DESIGN AUTOMATION [16MT25704]
28/07/2017 2:30PM TO 4:30PM	FRIDAY	WIRELESS COMMUNICATIONS [16MT23805]	WIRELESS NETWORKS [16MT26303]	WEB TECHNOLOGIES [16MT22505]	WIRELESS COMMUNICATIONS [16MT23805]	STATIC AND DIGITAL PROTECTION OF POWER SYSTEM [16MT20705]	WEB TECHNOLOGIES [16MT22505]	TESTING AND TESTABILITY [16MT25705]
29/07/2017 2:30PM TO 4:30PM	SATURDAY	RADAR SIGNAL PROCESSING [16MT26104]	COMPUTER FORENSICS [16MT26304]	MOBILE COMPUTING [16MT20506]	OPTICAL COMMUNICATIONS AND NETWORKS [16MT23807]	POWER QUALITY [16MT20708]	SOFTWARE PROCESS AND PROJECT MANAGEMENT [16MT22506]	SYSTEM-ON-CHIP DESIGN AND VERIFICATION [16MT25708]

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
2. Examination will be on the Units prescribed in the Syllabus Book.
3. College buses will leave the college with in 15 minutes after Examination Time.

Copy to: 1) The Director, SVEC.

2) The HODs of EEE, ECE, CSE & IT: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.

3) The Examination Section.

4) The S.A.O, for information and necessary transport arrangement.


PRINCIPAL
