Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh
Polytechnic Diploma V Semester (C-14) Unit test -II Examinations, October-2017
TIME - TABLE

Date: 16/09/2017

Time: 03:00 PM TO 04:00 PM

EXAM DATE & TIME	WEEK DAY	DIPLOMA-DCE	DIPLOMA-DCME	DIPLOMA-DECE	DIPLOMA-DEEE	DIPLOMA-DME
05/10/2017 3:00PM TO 4:00PM	THURSDAY	DESIGN & DETAILING OF R.C.ELEMENTS [C501]	JAVA PROGRAMMING [CM501]	ADVANCED COMMUNICATIONS [EC501]	ELECTRICAL UTILIZATION [EE501]	INDUSTRIAL MANAGEMENT [M501]
06/10/2017 3:00PM TO 4:00PM	FRIDAY	ENVIRONMENTAL ENGINEERING-I [C502]	SOFTWARE ENGINEERING [CM502]	CONSUMER ELECTRONICS [EC502]	A.C.MACHINES - II [EE502]	DESIGN OF MACHINI ELEMENTS -II [M502]
09/10/2017 3:00PM TO 4:00PM	MONDAY	QUANTITY SURVEYING-II	ADVANCED DATABASE SYSTEMS	COMPUTER HARDWARE	POWER SYSTEMS – II (T&D)	ESTIMATING & COSTING
		[C503]	[CM503]	[EC503]	[EE503]	[M503]
10/10/2017 3:00PM TO 4:00PM	TUESDAY	IRRIGATION ENGINEERING	WEB DESIGNING	OPTICAL FIBRE COMMUNICATION	INDUSTRIAL DRIVES	HEAT POWER ENGINEERING-II
		[C504]	[CM504]	[EC504]	[EE504]	[M504]
11/10/2017 3:00PM TO 4:00PM	WEDNESDAY	PROJECT MANAGEMENT FOR CONSTRUCTION	DATA COMMUNICATION	MICROCONTROLLER APPLICATIONS	DIGITAL ELECTRONICS	FLUID POWER CONTROL SYSTEM
		[C505]	[CM505]	[EC505]	[EE505]	[M505]
12/10/2017 3:00PM TO 4:00PM	THURSDAY		CLOUD COMPUTING	DATA COMMUNICATION & COMPUTER NETWORKING	MAINTENANCE OF ELECTRICAL SYSTEMS	PRODUCTION TECHNOLOGY-III
			[CM506]	[EC506]	[EE506]	[M506]

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.

2. Examination will be on the Units prescribed in the Syllabus Book.

Copy to: 1) The Director, SVEC.

2) The Vice Principal, 2nd Shift Polytechnic: With a request to arrange to read out in the respective classes and to display on the Notice Board.

3) S.A.O. for Information.

4) The Exam Section.

5) Notice Board.

PRINCIPAL