

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, Tirupati – 517 102  
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

**I B.Tech (SVEC-14) Supplementary Examinations December, 2017**

## EXAMINATION SCHEDULE

Date: 03-10-2017

Time: 02.00 PM To 05.00 PM

For 2014 & 2015 Admitted Batches only

DATE /DAY	For 2014 & 2015 Admitted Batches only						
	CIVIL	M.E	E.E.E	E.C.E	E.I.E	C.S.E	I.T
15/12/2017 FRIDAY	TECHNICAL ENGLISH (14BT1HS01)						
18/12/2017 MONDAY	ENGINEERING PHYSICS (14BT1BS01)						
20/12/2017 WEDNESDAY	ENGINEERING CHEMISTRY (14BT1BS02)						
22/12/2017 FRIDAY	ENGINEERING MATHEMATICS (14BT1BS03)						
26/12/2017 TUESDAY	ENGINEERING MECHANICS (14BT10301)		MATHEMATICAL METHODS (14BT1BS04)				
28/12/2017 THURSDAY	PROGRAMMING IN C AND DATA STRUCTURES (14BT1ES01)				PROBLEM SOLVING AND COMPUTER PROGRAMMING (14BT1ES02)		

  
CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HODs, of CE, M.E, EEE, ECE, CSE, EIE, IT, CSSE & BS&H: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The S.A.O, SVEC; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.