

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, Tirupati – 517 102
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

I B.Tech II Semester (SVEC-16) Supplementary Examinations December, 2017

EXAMINATION SCHEDULE

Date: 03-10-2017
Time: 02:00 PM To 05:00 PM

EXAM DATE	For 2016 Admitted Batch only							
	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
15/12/2017 FRIDAY	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]	TECHNICAL ENGLISH [16BT1HS01]	TECHNICAL ENGLISH [16BT1HS01]	TECHNICAL ENGLISH [16BT1HS01]	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]
18/12/2017 MONDAY	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]
20/12/2017 WEDNESDAY	BUILDING MATERIAL AND CONSTRUCTION TECHNOLOGY [16BT20101]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	ENGINEERING MATERIALS [16BT20301]
22/12/2017 FRIDAY	ENGINEERING MECHANICS [16BT20102]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	ENGINEERING CHEMISTRY [16BT1BS01]	ENGINEERING CHEMISTRY [16BT1BS01]	ENGINEERING CHEMISTRY [16BT1BS01]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	ENGINEERING MECHANICS [16BT20102]
26/12/2017 TUESDAY	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241]	DIGITAL LOGIC DESIGN [16BT21501]	DIGITAL LOGIC DESIGN [16BT21501]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	DIGITAL LOGIC DESIGN [16BT21501]	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241]

P. C. Srinivas
CHIEF CONTROLLER OF EXAMINATIONS

Copy to: The Director, SVEC.

The HODs, of CE, M.E, EEE, ECE, CSE, EIE, IT, CSSE & BS&H: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The Examination Section; The S.A.O, SVEC; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.