

SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

II B.Tech I Semester (SVEC-16) Regular Examinations November, 2017

EXAMINATION SCHEDULE

Date: 03-10-2017

Time: 10.00 AM To 01.00 PM

DATE /DAY	For 2016 Regular and 2017 Lateral Entry Admitted Batches only							
	CE	M.E	E.E.E	E.C.E	C.S.E	E.I.E	I.T	C.S.S.E
27/11/2017 MONDAY	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]
29/11/2017 WEDNESDAY	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY [16BT3HS02]	ENGINEERING METALLURGY [16BT30301]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	COMPUTER ORGANIZATION [16BT30501]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	COMPUTER ORGANIZATION [16BT30501]	COMPUTER ORGANIZATION [16BT30501]
04/12/2017 MONDAY	CONSTRUCTION PLANNING AND PROJECT MANAGEMENT [16BT30101]	KINEMATICS OF MACHINERY [16BT30302]	DC MACHINES [16BT30201]	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN [16BT30401]	DATA STRUCTURES [16BT30502]	ELECTRICAL AND ELECTRONIC MEASUREMENTS [16BT31001]	DATA STRUCTURES [16BT30502]	DATA STRUCTURES [16BT30502]
06/12/2017 WEDNESDAY	FLUID MECHANICS AND HYDRAULIC MACHINERY [16BT30102]	MANUFACTURING TECHNOLOGY [16BT30303]	ELECTROMAGNETIC FIELDS [16BT30202]	SIGNALS AND SYSTEMS [16BT30402]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	SENSORS AND TRANSDUCERS [16BT31002]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]
08/12/2017 FRIDAY	MECHANICS OF SOLIDS [16BT30103]	STRENGTH OF MATERIALS [16BT30304]	SIGNALS, SYSTEMS AND NETWORKS [16BT30203]	SWITCHING THEORY AND LOGIC DESIGN [16BT30403]	PYTHON PROGRAMMING [16BT30504]	SWITCHING THEORY AND LOGIC DESIGN [16BT30403]	LINUX PROGRAMMING [16BT30503]	JAVA PROGRAMMING [16BT41202]
11/12/2017 MONDAY	SURVEYING [16BT30104]	THERMODYNAMICS [16BT30305]	ANALOG ELECTRONIC CIRCUITS [16BT30441]	ELECTRICAL TECHNOLOGY [16BT30241]	OPERATING SYSTEMS [16BT31501]	ELECTRICAL TECHNOLOGY [16BT30241]	OPERATING SYSTEMS [16BT31501]	OPERATING SYSTEMS [16BT31501]


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT & CSSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.