

SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

II B.Tech I Semester (SVEC-14) Supplementary Examinations November, 2017

EXAMINATION SCHEDULE

Date: 03-10-2017

Time: 10.00 AM To 01.00 PM

DATE /DAY	For 2014 , 2015 Regular and 2015 & 2016 Lateral Entry Admitted Batches only							
	CE	M.E	E.E.E	E.C.E	C.S.E	E.I.E	I.T	C.S.S.E
27/11/2017 MONDAY	MATRICES AND NUMERICAL METHODS 14BT3BS01	MATRICES AND NUMERICAL METHODS 14BT3BS01	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS 14BT3BS02	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS 14BT3BS02	PROBABILITY AND STATISTICS 14BT3BS03	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS 14BT3BS02	PROBABILITY AND STATISTICS 14BT3BS03	PROBABILITY AND STATISTICS 14BT3BS03
29/11/2017 WEDNESDAY	ENGINEERING GEOLOGY 14BT30101	BASICS OF ELECTRICAL AND ELECTRONICS ENGINEERING 14BT30234	SEMICONDUCTOR DEVICES AND CIRCUITS 14BT30402	SEMICONDUCTOR DEVICES AND CIRCUITS 14BT30402	DATA STRUCTURES 14BT30501	SEMICONDUCTOR DEVICES AND CIRCUITS 14BT30402	DATA STRUCTURES 14BT30501	DATA STRUCTURES 14BT30501
04/12/2017 MONDAY	BASICS OF ELECTRICAL AND MECHANICAL TECHNOLOGY 14BT30233	STRENGTH OF MATERIALS 14BT30301	ENVIRONMENTAL SCIENCES 14BT3HS01	ENVIRONMENTAL SCIENCES 14BT3HS01	BASIC ELECTRICAL ENGINEERING 14BT30235	ENVIRONMENTAL SCIENCES 14BT3HS01	BASIC ELECTRICAL ENGINEERING 14BT30235	FOUNDATIONS OF ELECTRICAL ENGINEERING 14BT30236
06/12/2017 WEDNESDAY	MECHANICS OF SOLIDS 14BT30102	MATERIALS SCIENCE AND METALLURGY 14BT30302	ELECTROMAGNETIC FIELDS 14BT30201	ELECTRICAL TECHNOLOGY 14BT30231	DIGITAL LOGIC DESIGN 14BT30502	PRINCIPLES OF ELECTRICAL MEASUREMENTS 14BT31001	DIGITAL LOGIC DESIGN 14BT30502	DIGITAL LOGIC DESIGN 14BT30502
08/12/2017 FRIDAY	BUILDING MATERIALS AND CONCRETE TECHNOLOGY 14BT30103	THERMODYNAMICS 14BT30303	DC MACHINES 14BT30202	PROBABILITY AND STOCHASTIC PROCESSES 14BT30401	DISCRETE MATHEMATICAL STRUCTURES 14BT31201	SENSORS AND TRANSDUCERS 14BT31002	DISCRETE MATHEMATIC AL STRUCTURES 14BT31201	DISCRETE MATHEMATICAL STRUCTURES 14BT31201
11/12/2017 MONDAY	FLUID MECHANICS-I 14BT30104	MANUFACTURING TECHNOLOGY-I 14BT30304	ELECTRIC CIRCUITS 14BT30203	NETWORK ANALYSIS 14BT30232	ELECTRONIC DEVICES AND CIRCUITS 14BT30431	NETWORK ANALYSIS 14BT30232	ELECTRONIC DEVICES AND CIRCUITS 14BT30431	ELECTRONIC DEVICES AND CIRCUITS 14BT30431

P. V. Venkatesh
CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT & CSSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.