

SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Ananthapuramu)

III B.Tech I Semester (SVEC-14) Regular/Supplementary Examinations November, 2017

Date: 03-10-2017

Time: 10.00 AM To 01.00 PM

EXAMINATION SCHEDULE

For 2014, 2015 Regular and 2015 & 2016 Lateral Entry Admitted Batch Students

DATE /DAY	CE	EEE	ME	ECE	CSE	EIE	IT	CSSE
28/11/2017 TUESDAY	MANAGEMENT SCIENCE (14BT5HS02)	BUSINESS COMMUNICATION AND PRESENTATION SKILLS (14BT4HS01)	DYNAMICS OF MACHINERY (14BT50301)	BUSINESS COMMUNICATION AND PRESENTATION SKILLS (14BT4HS01)	MANAGEMENT SCIENCE (14BT5HS02)	BUSINESS COMMUNICATION AND PRESENTATION SKILLS (14BT4HS01)	MANAGEMENT SCIENCE (14BT5HS02)	MANAGEMENT SCIENCE (14BT5HS02)
30/11/2017 THURSDAY	PROFESSIONAL ETHICS (14BT4HS02)	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY (14BT5HS01)	PROFESSIONAL ETHICS (14BT4HS02)	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY (14BT5HS01)	PROFESSIONAL ETHICS (14BT4HS02)	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY (14BT5HS01)	PROFESSIONAL ETHICS (14BT4HS02)	PROFESSIONAL ETHICS (14BT4HS02)
05/12/2017 TUESDAY	STRUCTURAL ANALYSIS - II (14BT50101)	CONTROL SYSTEMS (14BT50201)	INDUSTRIAL ENGINEERING AND MANAGEMENT (14BT50302)	ANTENNAS AND PROPAGATION (14BT50401)	THEORY OF COMPUTATION (14BT50501)	ELECTRONIC INSTRUMENTATION (14BT51001)	THEORY OF COMPUTATION (14BT50501)	DESIGN AND ANALYSIS OF ALGORITHMS (14BT40503)
07/12/2017 THURSDAY	REINFORCED CEMENT CONCRETE STRUCTURES - I (14BT50102)	COMPUTER ORGANIZATION AND ARCHITECTURE (14BT50202)	THERMAL ENGINEERING - II (14BT50303)	DIGITAL COMMUNICATIONS (14BT50402)	UNIX INTERNALS (14BT50502)	COMPUTER ORGANIZATION AND ARCHITECTURE (14BT50202)	UNIX INTERNALS (14BT50502)	SYSTEMS SOFTWARE (14BT51502)
09/12/2017 SATURDAY	WATER SUPPLY ENGINEERING (14BT50103)	SYNCHRONOUS MACHINES (14BT50203)	MACHINE TOOLS (14BT50304)	DIGITAL IC APPLICATIONS (14BT50403)	SOFTWARE ENGINEERING (14BT51202)	INDUSTRIAL INSTRUMENTATION - I (14BT51002)	SOFTWARE ENGINEERING (14BT51202)	SOFTWARE ENGINEERING (14BT51202)
12/12/2017 TUESDAY	SOIL MECHANICS (14BT50104)	TRANSMISSION OF ELECTRIC POWER (14BT50204)	DESIGN OF MACHINE ELEMENTS-I (14BT50305)	LINEAR IC APPLICATIONS (14BT50404)	OPERATING SYSTEMS (14BT51501)	DIGITAL SIGNAL PROCESSING (14BT60401)	COMPUTER NETWORKS (14BT51201)	OPERATING SYSTEMS (14BT51501)
14/12/2017 THURSDAY	ENGINEERING HYDROLOGY (14BT50105)	LINEAR AND DIGITAL IC APPLICATIONS (14BT51003)	AUTOMOBILE ENGINEERING (14BT50306)	PULSE AND DIGITAL CIRCUITS (14BT50405)	MICROPROCESSORS AND INTERFACING (14BT50431)	LINEAR AND DIGITAL IC APPLICATIONS (14BT51003)	MICROPROCESSORS AND INTERFACING (14BT50431)	MICROPROCESSORS AND INTERFACING (14BT50431)


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HODs, of CE, EEE, ME, ECE, CSE, EIE, IT & CSSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The Examination Section.

The S.A.O, SVEC; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.