

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

## III B.Tech II Semester (SVEC-10) Supplementary Examinations December, 2017

### EXAMINATION SCHEDULE

Date: 03-10-2017

Time: 10.00 AM To 01.00 PM

| DATE /DAY               | For 2010,2011,2012 & 2013 Regular and 2012,2013 & 2014 Lateral Entry Admitted Batches only |   |  |   |  |   |  |   |  |  |
|-------------------------|--|---|--|---|--|---|--|---|--|--|
|                         | CIVIL  | E.E.E   | M.E  | E.C.E   | C.S.E  | E.I.E   | I.T  | E.Con.E   | C.S.S.E  | Bio-Tech                                     |
| 15/12/2017<br>FRIDAY    | ESTIMATION AND QUANTITY SURVEYING<br>10BT60101   | MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY<br>10BT4HS01 | OPERATIONS RESEARCH<br>10BT60301           | MANAGEMENT SCIENCE<br>10BT6HS01                   | THEORY OF COMPUTATION<br>10BT60501               | MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY<br>10BT4HS01 | PRINCIPLES OF COMPILER DESIGN<br>10BT70501       | MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY<br>10BT4HS01 | PRINCIPLES OF COMPILER DESIGN<br>10BT70501       | MANAGEMENT SCIENCE<br>10BT6HS01              |
| 18/12/2017<br>MONDAY    | STEEL STRUCTURES - I<br>10BT60102  | DIGITAL SIGNAL PROCESSING<br>10BT60401                          | METROLOGY & MEASUREMENTS<br>10BT60302      | DIGITAL SIGNAL PROCESSING<br>10BT60401            | UNIX PROGRAMMING<br>10BT60502                    | DIGITAL SIGNAL PROCESSING<br>10BT60401                          | UNIX PROGRAMMING<br>10BT60502                    | DIGITAL SIGNAL PROCESSING<br>10BT60401                          | PRINCIPLES OF PROGRAMMING LANGUAGES<br>10BT40503 | BIOINFORMATICS<br>10BT62301                  |
| 20/12/2017<br>WEDNESDAY | WATER RESOURCES ENGINEERING<br>10BT60103   | MICROPROCESSORS AND MICROCONTROLLERS<br>10BT60404               | HEAT TRANSFER<br>10BT60303                 | MICROPROCESSORS AND MICROCONTROLLERS<br>10BT60404 | DATA WAREHOUSING AND DATA MINING<br>10BT60503    | MICROPROCESSORS AND MICROCONTROLLERS<br>10BT60404               | DATA WAREHOUSING AND DATA MINING<br>10BT60503    | MICROPROCESSORS AND MICROCONTROLLERS<br>10BT60404               | COMPUTER GRAPHICS<br>10BT51202                   | IMMUNOLOGY<br>10BT62302                      |
| 22/12/2017<br>FRIDAY    | ENVIRONMENTAL ENGINEERING - I<br>10BT60104   | LINEAR AND DIGITAL IC APPLICATIONS<br>10BT50422                 | CAD/CAM<br>10BT60304                       | DIGITAL COMMUNICATIONS<br>10BT60402               | OBJECT ORIENTED ANALYSIS AND DESIGN<br>10BT61201 | PRINCIPLES OF COMMUNICATIONS<br>10BT50423                       | OBJECT ORIENTED ANALYSIS AND DESIGN<br>10BT61201 | OPTIMIZATION TECHNIQUES<br>10BT4EC01                            | WEB PROGRAMMING<br>10BT71201                     | ENZYME ENGINEERING<br>10BT62303              |
| 26/12/2017<br>TUESDAY   | TRANSPORTATION ENGINEERING<br>10BT60105  | UTILIZATION OF ELECTRICAL ENERGY<br>10BT60201                   | DESIGN OF MACHINE ELEMENTS-II<br>10BT60305 | MICROWAVE ENGINEERING<br>10BT60403                | DISTRIBUTED COMPUTING<br>10BT60504               | OPTOELECTRONIC AND LASER INSTRUMENTATION<br>10BT61001           | COMPUTER NETWORKS<br>10BT61202                   | DIGITAL CONTROL SYSTEMS<br>10BT61301                            | COMPUTER NETWORKS<br>10BT61202                   | APPLIED TRANSPORT PHENOMENA<br>10BT62304     |
| 28/12/2017<br>THURSDAY  | FOUNDATION ENGINEERING<br>10BT60106  | POWER SEMICONDUCTOR DRIVES<br>10BT60202                         | AUTOMOBILE ENGINEERING<br>10BT60306        | VLSI DESIGN<br>10BT60405                          | CRYPTOGRAPHY AND NETWORK SECURITY<br>10BT71204   | BIOMEDICAL INSTRUMENTATION<br>10BT61002                         | DESIGN AND ANALYSIS OF ALGORITHMS<br>10BT50501   | ROBOTICS AND AUTOMATION<br>10BT61302                            | MODELING AND SIMULATION<br>10BT61501             | BIOPROCESS DYNAMICS AND CONTROL<br>10BT62305 |

  
CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT and CSSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.