



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

Date: 12.10.2017

M.C.A V Semester (SVEC 10) Supplementary Examinations November, 2017.

EXAMINATION SCHEDULE

Time: 02.00 PM To 05.00PM

Date/Day	For 2012 & 2013 Admitted Batches only
28.11.2017 Tuesday	OBJECT ORIENTED ANALYSIS AND DESIGN 10MC50101
30.11.2017 Thursday	MANAGEMENT INFORMATION SYSTEMS 10MC50102
05.12.2017 Tuesday	MIDDLEWARE TECHNOLOGIES 10MC50103
07.12.2017 Thursday	SOFTWARE TESTING 10MC50106
09.12.2017 Saturday	CLOUD COMPUTING 10MC50108

Note: Laboratory examinations will be conducted from 18.11.2017 to 25.11.2017.


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HOD, MCA : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The S.A.O, SVEC.

The Manager, Andhra Bank, Sri Vidyanikethan Branch.
Hostel Notice Boards.