

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh
II B.Tech I Semester (SVEC-16) Mid-II Examinations, November-2017

TIME - TABLE

Date : 23/10/2017

Time : 10:30 AM TO 12:30 PM

EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
06/11/2017 10:30AM TO 12:30PM	MONDAY	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]
07/11/2017 10:30AM TO 12:30PM	TUESDAY	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY [16BT3HS02]	COMPUTER ORGANIZATION [16BT30501]	COMPUTER ORGANIZATION [16BT30501]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	COMPUTER ORGANIZATION [16BT30501]	ENGINEERING METALLURGY [16BT30301]
08/11/2017 10:30AM TO 12:30PM	WEDNESDAY	CONSTRUCTION PLANNING AND PROJECT MANAGEMENT [16BT30101]	DATA STRUCTURES [16BT30502]	DATA STRUCTURES [16BT30502]	ELECTRONIC CIRCUIT ANALYSIS & DESIGN [16BT30401]	DC MACHINES [16BT30201]	ELECTRICAL AND ELECTRONIC MEASUREMENTS [16BT31001]	DATA STRUCTURES [16BT30502]	KINEMATICS OF MACHINERY [16BT30302]
09/11/2017 10:30AM TO 12:30PM	THURSDAY	FLUID MECHANICS AND HYDRAULIC MACHINERY [16BT30102]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	SIGNALS AND SYSTEMS [16BT30402]	ELECTROMAGNETIC FIELDS [16BT30202]	SENSORS AND TRANSDUCERS [16BT31002]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	MANUFACTURING TECHNOLOGY [16BT30303]
10/11/2017 10:30AM TO 12:30PM	FRIDAY	MECHANICS OF SOLIDS [16BT30103]	PYTHON PROGRAMMING [16BT30504]	JAVA PROGRAMMING [16BT41202]	SWITCHING THEORY AND LOGIC DESIGN [16BT30403]	SIGNALS, SYSTEMS AND NETWORKS [16BT30203]	SWITCHING THEORY AND LOGIC DESIGN [16BT30403]	LINUX PROGRAMMING [16BT30503]	STRENGTH OF MATERIALS [16BT30304]
11/11/2017 10:30AM TO 12:30PM	SATURDAY	SURVEYING [16BT30104]	OPERATING SYSTEMS [16BT31501]	OPERATING SYSTEMS [16BT31501]	ELECTRICAL TECHNOLOGY [16BT30241]	ANALOG ELECTRONIC CIRCUITS [16BT30441]	ELECTRICAL TECHNOLOGY [16BT30241]	OPERATING SYSTEMS [16BT31501]	THERMODYNAMICS [16BT30305]

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
2. Examination will be on the Units prescribed in the Syllabus Book.
3. College buses will leave the college with in 15 minutes after Examination Time.


PRINCIPAL
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- Copy to: 1) The Director, SVEC.
2) The HODs of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & BS&H: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.
3) The Examination Section.
4) The S.A.O, for information and necessary transport arrangement.