SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati– 517 102

Date: 05.12.2017

for M.C.A II Semester (SVEC 10) Supplementary Examinations January,2018

EXAMINATION SCHEDULE

Time: 02.00 PM To 05.00PM

Date/Day	For 2012 & 2013 Admitted Batches only
23.01.2018 Tuesday	PROBABILITY AND STATISTICS 10MC2BS01
25.01.2018 Thursday	COMPUTER ARCHITECTURE AND ORGANIZATION 10MC20101
29.01.2018 Monday	OPERATING SYSTEMS 10MC20102
31.01.2018 Wednesday	OBJECT ORIENTED PROGRAMMING 10MC20103
02.02.2018 Friday	DATABASE MANAGEMENT SYSTEM 10MC20104

Note: Laboratory examinations will be conducted from 08.01.2018 to 10.01.2018.

CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director. SVEC.
The HOD, MCA : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.
The Examination Section.
The S.A.O, SVEC.
SVEC Canteen.
Estate Manager.
Hostel Notice Boards.