



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

Date: 05.12.2017

M.C.A III Semester (SVEC 10) Supplementary Examinations January, 2018

EXAMINATION SCHEDULE

Time: 02.00 PM To 05.00PM

Date/Day	For 2012 & 2013 Admitted Batches only
22.01.2018 Monday	OPERATIONS RESEARCH 10MC3BS01
24.01.2018 Wednesday	WEB APPLICATION DEVELOPMENT 10MC30101
27.01.2018 Saturday	COMPUTER NETWORKS 10MC30102
30.01.2018 Tuesday	DATA WAREHOUSING AND DATA MINING 10MC30103
01.02.2018 Thursday	SOFTWARE ENGINEERING 10MC30104

Note: Laboratory examinations will be conducted from 08.01.2018 to 10.01.2018.


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director. SVEC.

The HOD, MCA : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The S.A.O, SVEC.

SVEC Canteen.

Estate Manager.

Hostel Notice Boards.