

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh
II B.Tech II Semester (SVEC-16) Mid-I Examinations, February-2018


TIME - TABLE

Date : 07/02/2018

Time : 10:30 AM To 12:30 PM

EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
22/02/2018 10:30AM To 12:30PM	THURSDAY	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ANALOG COMMUNICATIONS [16BT40401]	ELECTRICAL MEASUREMENTS [16BT40201]	CONTROL SYSTEMS [16BT50201]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]
23/02/2018 10:30AM To 12:30PM	FRIDAY	CONCRETE TECHNOLOGY [16BT40101]	COMPUTER GRAPHICS [16BT40501]	THEORY OF COMPUTATION [16BT41204]	DIGITAL IC APPLICATIONS [16BT40402]	GENERATION OF ELECTRIC POWER [16BT40202]	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN [16BT30401]	THEORY OF COMPUTATION [16BT41204]	DESIGN OF MACHINE ELEMENTS-I [16BT40301]
24/02/2018 10:30AM To 12:30PM	SATURDAY	ENGINEERING GEOLOGY [16BT40102]	DATABASE MANAGEMENT SYSTEMS [16BT40502]	DATABASE MANAGEMENT SYSTEMS [16BT40502]	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES [16BT40403]	TRANSFORMERS AND INDUCTION MACHINES [16BT40203]	INDUSTRIAL INSTRUMENTATION-I [16BT41001]	DATABASE MANAGEMENT SYSTEMS [16BT40502]	DYNAMICS OF MACHINERY [16BT40302]
26/02/2018 10:30AM To 12:30PM	MONDAY	ENGINEERING HYDROLOGY [16BT40103]	DESIGN AND ANALYSIS OF ALGORITHMS [16BT41201]	DESIGN AND ANALYSIS OF ALGORITHMS [16BT41201]	LINEAR IC APPLICATIONS [16BT40404]	LINEAR AND DIGITAL ICS [16BT41002]	LINEAR AND DIGITAL ICS [16BT41002]	DESIGN AND ANALYSIS OF ALGORITHMS [16BT41201]	FLUID MECHANICS [16BT40303]
27/02/2018 10:30AM To 12:30PM	TUESDAY	STRUCTURAL ANALYSIS-I [16BT40104]	JAVA PROGRAMMING [16BT41202]	OBJECT ORIENTED ANALYSIS AND DESIGN [16BT51202]	PROBABILITY AND STOCHASTIC PROCESS [16BT40405]	SWITCHING THEORY AND LOGIC DESIGN [16BT30403]	SIGNALS AND SYSTEMS [16BT30402]	JAVA PROGRAMMING [16BT41202]	MACHINE TOOLS AND MODERN MACHINING PROCESS [16BT40304]
28/02/2018 10:30AM To 12:30PM	WEDNESDAY	WATER SUPPLY ENGINEERING [16BT40105]	SOFTWARE ENGINEERING [16BT41203]	SOFTWARE ENGINEERING [16BT41203]	PULSE AND DIGITAL CIRCUITS [16BT40406]	COMPUTER ARCHITECTURE AND ORGANIZATION [16BT41041]	PULSE AND DIGITAL CIRCUITS [16BT40406]	SOFTWARE ENGINEERING [16BT41203]	THERMAL ENGINEERING - I [16BT40305]

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
2. Examination will be on the Units prescribed in the Syllabus Book.
3. College buses will leave the college with in 15 minutes after Examination Time.


PRINCIPAL

- Copy to: 1) The Director, SVEC.
2) The HODs of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & BS&H: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.
3) The Examination Section.
4) The S.A.O, for information and necessary transport arrangement.