

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

## II B.Tech I Semester (SVEC-10) Supplementary Examinations June, 2018

### EXAMINATION SCHEDULE

Date: 27-03-2018

Time: 10.00 AM To 01.00 PM

DATE /DAY	For 2010, 2011, 2012 & 2013 Regular and 2012, 2013 & 2014 Lateral Entry Admitted Batches only									
	CIVIL	M.E	E.E.E	E.C.E	C.S.E	E.I.E	I.T	E.Con.E	C.S.S.E	Bio-Tech
<b>18/06/2018 MONDAY</b>	MATRICES AND NUMERICAL METHODS 10BT3BS04	MATRICES AND NUMERICAL METHODS 10BT3BS04	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS 10BT3BS03	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS 10BT3BS03	PROBABILITY AND STATISTICS 10BT3BS01	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS 10BT3BS03	PROBABILITY AND STATISTICS 10BT3BS01	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS 10BT3BS03	PROBABILITY AND STATISTICS 10BT3BS01	MATRICES AND NUMERICAL METHODS 10BT3BS04
<b>20/06/2018 WEDNESDAY</b>	MECHANICS OF SOLIDS 10BT30101	STRENGTH OF MATERIALS 10BT30301	SEMICONDUCTOR DEVICES AND CIRCUITS 10BT30401	SEMICONDUCTOR DEVICES AND CIRCUITS 10BT30401	ELECTRONIC DEVICES AND CIRCUITS 10BT30421	SEMICONDUCTOR DEVICES AND CIRCUITS 10BT30401	ELECTRONIC DEVICES AND CIRCUITS 10BT30421	SEMICONDUCTOR DEVICES AND CIRCUITS 10BT30401	ELECTRONIC DEVICES AND CIRCUITS 10BT30421	BIO CHEMISTRY 10BT32301
<b>22/06/2018 FRIDAY</b>	BUILDING MATERIALS AND CONCRETE TECHNOLOGY 10BT30102	BASICS OF ELECTRICAL AND ELECTRONICS ENGINEERING 10BT30224	FLUID MECHANICS AND HYDRAULIC MACHINERY 10BT30121	CIRCUIT THEORY 10BT30223	BASIC ELECTRICAL ENGINEERING 10BT30221	CIRCUIT THEORY 10BT30223	BASIC ELECTRICAL ENGINEERING 10BT30221	CIRCUIT THEORY 10BT30223	FOUNDATIONS OF ELECTRICAL ENGINEERING 10BT30222	CELL BIOLOGY 10BT32302
<b>25/06/2018 MONDAY</b>	BASICS OF ELECTRICAL AND MECHANICAL TECHNOLOGY 10BT30103	MATERIALS SCIENCE AND METALLURGY 10BT30302	SWITCHING THEORY & LOGIC DESIGN 10BT40404	ENVIRONMENTAL SCIENCE 10BT3BS02	DIGITAL LOGIC DESIGN 10BT30422	SWITCHING THEORY & LOGIC DESIGN 10BT40404	DIGITAL LOGIC DESIGN 10BT30422	SWITCHING THEORY & LOGIC DESIGN 10BT40404	DIGITAL LOGIC DESIGN 10BT30422	ENVIRONMENTAL SCIENCE 10BT3BS02
<b>27/06/2018 WEDNESDAY</b>	SURVEYING 10BT30104	THERMODYNAMICS 10BT30303	ELECTRICAL CIRCUITS 10BT30201	PROBABILITY THEORY AND STOCHASTIC PROCESSES 10BT30402	DISCRETE MATHEMATICAL STRUCTURES 10BT30501	THERMODYNAMICS AND FLUID MECHANICS 10BT31001	DISCRETE MATHEMATICAL STRUCTURES 10BT30501	ELECTRICAL & ELECTRONIC MEASUREMENTS 10BT41001	DISCRETE MATHEMATICAL STRUCTURES 10BT30501	MICRO BIOLOGY 10BT32303
<b>29/06/2018 FRIDAY</b>	FLUID MECHANICS-I 10BT30105	** MACHINE DRAWING 10BT30304	DC MACHINES 10BT30202	SIGNALS AND SYSTEMS 10BT30403	DATA STRUCTURES 10BT30502	SIGNALS AND SYSTEMS 10BT30403	DATA STRUCTURES 10BT30502	SIGNALS AND SYSTEMS 10BT30403	DATA STRUCTURES 10BT30502	FLUID FLOW IN BIOPROCESSES 10BT32304

\*\* Examination Time is from 10.00 AM to 2.00 PM

Copy to : The Director, SVEC

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT & CSSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.

  
CHIEF CONTROLLER OF EXAMINATIONS