

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh
I B.Tech II Semester (SVEC-16) Supplementary Examinations, June 2018

TIME - TABLE

Date : 09/04/2018

Time : 10:00 AM To 01:00 PM

EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
05/06/2018 10:00AM TO 1:00PM	TUESDAY	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]	TECHNICAL ENGLISH [16BT1HS01]	TECHNICAL ENGLISH [16BT1HS01]	TECHNICAL ENGLISH [16BT1HS01]	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]
07/06/2018 10:00AM TO 1:00PM	THURSDAY	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]
09/06/2018 10:00AM TO 1:00PM	SATURDAY	BUILDING MATERIAL AND CONSTRUCTION TECHNOLOGY [16BT20101]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	ENGINEERING MATERIALS [16BT20301]
12/06/2018 10:00AM TO 1:00PM	TUESDAY	ENGINEERING MECHANICS [16BT20102]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	ENGINEERING CHEMISTRY [16BT1BS01]	ENGINEERING CHEMISTRY [16BT1BS01]	ENGINEERING CHEMISTRY [16BT1BS01]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	ENGINEERING MECHANICS [16BT20102]
14/06/2018 10:00AM TO 1:00PM	THURSDAY	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241]	DIGITAL LOGIC DESIGN [16BT21501]	DIGITAL LOGIC DESIGN [16BT21501]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	DIGITAL LOGIC DESIGN [16BT21501]	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241]


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.